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The Previous Version End (PVE) date is the date when the transition period ends. After the PVE Date, “New” devices can no longer be certified to version 1.1 of the LXI Specification. For Version 1.2 of the LXI Specification, the Previous Version End Date is 31-May-2008.

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Several sections of the specification highlight “Roadmap” items. These items have been identified by Consortium members as deserving consideration for inclusion in a future version of the specification. They are included to signal the Consortium’s interest in the area and to alert implementers of possible areas of change.

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Secretary, LXI Consortium Standards Board
www.lxistandard.org
## Revision history

<table>
<thead>
<tr>
<th>Revision</th>
<th>Description</th>
</tr>
</thead>
</table>
| 1.1 Apr 2006 | For a complete list of changes between 1.0 and 1.1, please refer to the 1.1 version of the standard. The following is a summary of some of the most salient changes for easy reference.  
| | • Over-Current Protection (2.7.7.1) changed to a recommendation  
| | • Wired OR behavior modified to disallow bias source to signal  
| | • Event Log requirements clarified  
| | • Rule 8.6 deprecated and LAN Configuration and Status behaviors clarified (Section 8)  
| | • Minimum HTTP version modified to 1.0 and XHTML permitted (Section 9)  
| | • Conformance requirements and processes clarified (Section 14)  
| | • Hybrid Systems clarifications added (Section 15)  
| | • Cable and terminator specification (Appendix C) moved to separate document  
| | Notice of Effective Dates and Grandfathering added to document. Dropped references to Version 1.0 from testing requirements.  
| | Added descriptions of roadmap items and future rules.  
| | 2.8.3.2, 2.8.3.2.1, and 9.6 – Clarified LED usage for IEEE 1588 clock status  
| | 3.1 - Added Roadmap item to Section 3.1 regarding conformance with IEEE 1588-2008, which will be required shortly after that version is approved by the IEEE.  
| | 3.3.1 – Explained reason for deprecating this rule.  
| | 3.3.2 – Clarified the meaning and behavior of LAN event timestamp, T1, offset/delay, and T2 in Section 3.3.2. Reorganized itemized lists for parallel structure.  
| | 3.4 – Clarified requirements for time-based triggering.  
| | 3.10 – Narrowed requirements for internal event logs to module-to-module LAN messages. Changed reference in Observation from Recommendation 3.10 to Rule 3.10. Added Recommendation 3.10.1 to define significant events that should also be logged.  
| | 3.12 – Added future rule for Pulse-Per-Second Output.  
| | 4.4 – Clarified the following terms: eventID field, Sequence, UInteger. Deprecated use of Bit 1 for retransmission. Clarified reference to table in section 6.4.4. Clarified use of Bit 2: Hardware Value and Bit 4 – Stateless Event.  
| | 4.4.4 – Deprecated the rule for retransmitted data packets; explanation of change added.  
| | 4.4.4.1 – Deprecated the rule for handling of retransmitted data packets; explanation of change added.  
| | 5.4.4 – Changed reference from LXISync Interface Specification to IVI-3.15: IVILxiSync Specification.  
| | 6.4.3 – Modified text to explain use of Flag Bit 2 and Flag Bit 4 in LAN events.  
| | 6.4.4 – Divided table of standard strings in two, one covering triggering and synchronization and one covering event generation. Added references to use of Flag Bit 4 in LAN event messages.  
<p>| | 6.7.1 – Modified the rule to cover LAN event interpolation and revised the text. Added an observation describing behavior improvements thus enabled. |</p>
<table>
<thead>
<tr>
<th>6.8</th>
<th>Updated the Event Log definition to meet Rule 3.10 and 6.8.1 requirements. Added cross-reference to 3.10.</th>
</tr>
</thead>
<tbody>
<tr>
<td>6.8.1</td>
<td>Clarified event log semantics to add FIFO buffer behavior and support for operation when the buffer is full.</td>
</tr>
<tr>
<td>8.14</td>
<td>Added 10.7.1 to table entry for multicast DNS (mDNS) and DNS service discovery (DNS-SD). Added note clarifying mDNS and DNS-SD requirements in Version 1.2.</td>
</tr>
<tr>
<td>9.15</td>
<td>Added new rule that reserves all URLs beginning with “LXI” in any combination of upper- and/or lowercase letters.</td>
</tr>
<tr>
<td>9.2.1 – 9.2.1.1 and Appendix A</td>
<td>Made changes to sections regarding InstrumentAddressString and related identification schemas.</td>
</tr>
<tr>
<td>9.6</td>
<td>Corrected table entry for “current observed variance of parent” and added new observation pertaining to the value. Added new entries for IEEE 1588 domain and LXI module-to-module parameters.</td>
</tr>
<tr>
<td>10</td>
<td>Changed chapter name to “LAN Discovery and Identification.” Added sections 10.2 through 10.2.4.2 pertaining to Identification Schemas. Removed roadmap item.</td>
</tr>
<tr>
<td>10.3 through 10.8</td>
<td>Added an extensive series of future rules and permissions for the support of mDNS and DNS-SD.</td>
</tr>
<tr>
<td>10.3.3.1 and following sections</td>
<td>Agreed upon use of “link local” host and service names.</td>
</tr>
<tr>
<td>10.3.4</td>
<td>Clarified wording of future rule on DHCP host name option.</td>
</tr>
<tr>
<td>10.4.3 and 10.4.3.1</td>
<td>Clarified use of empty TXT records.</td>
</tr>
<tr>
<td>10.7.1</td>
<td>Created future rule for hostname and service name default usage.</td>
</tr>
<tr>
<td>14.5.1.2 and 14.5.1.4</td>
<td>Deprecated these two rules specifying use of multiple devices for interoperability during conformance testing. Deprecated in favor of Rule 14.5.1.3.</td>
</tr>
<tr>
<td>1.2.01 Nov 2007</td>
<td>Fixed typographical error in Section 8.</td>
</tr>
</tbody>
</table>

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1 Overview and Class Definition

This standard has been written and is controlled by the members of LXI Consortium, a not-for-profit organization created for the development and promotion of a LAN (Ethernet) based standard for instrumentation and related peripheral devices. This LXI Standard details the technical requirements of LAN-based devices that are LXI conformant.

1.1 Intended Audience

This LXI Standard is intended for use by designers, integrators and users of devices that are designed to be LXI conformant.

1.2 Background and Terminology

LXI is an acronym for LAN eXtensions for Instrumentation. The LXI specification details the technical requirements of LXI devices using Ethernet as the primary communications bus between devices.

This standard makes use of a number of widely accepted acronyms that are defined in the glossary.

1.3 Purpose and Scope of this Document

This document defines a set of RULES and RECOMMENDATIONS for constructing an LXI conformant device that interfaces to a local area network with Ethernet protocols. The specification covers physical, functional, electrical, and software aspects of LXI conformant devices.

Key objectives in the development of this standard for test and measurement instrumentation have included:

1. Unambiguous communication amongst LXI devices
2. A reduction in the physical size of test systems
3. Decreasing the cost of test system software development by the use of industry-standard protocols and interfaces
4. Provision of a standardized trigger and synchronization mechanism between LXI devices
5. Increasing system performance by using high-speed, Ethernet protocols
6. Taking advantage of the simplicity of physical Ethernet connectivity, including the ability to seamlessly include optical and wireless connections
7. Supporting the use of synthetic instruments
8. Supporting the use of other device interfaces, where appropriate, in systems having LXI conformant elements
1.4 Definition of Terms

Throughout this document you will see the following headings on paragraphs. These headings identify the contents of the paragraph:

**RULE**: Rules **SHALL** be followed to ensure compatibility for LAN-based devices. A rule is characterized by the use of the words **SHALL** and **SHALL NOT**. These words are not used for any other purpose other than stating rules.

**RECOMMENDATION**: Recommendations consist of advice to implementers that will affect the usability of the final device. Discussions of particular hardware to enhance throughput would fall under a recommendation. These should be followed to avoid problems and to obtain optimum performance.

**SUGGESTION**: A suggestion contains advice that is helpful but not vital. The reader is encouraged to consider the advice before discarding it. Suggestions are included to help the novice designer with areas of design that can be problematic.

**PERMISSION**: Permissions are included to clarify the areas of the specification that are not specifically prohibited. Permissions reassure the reader that a certain approach is acceptable and will cause no problems. The word **MAY** is reserved for indicating permissions.

**OBSERVATION**: Observations spell out implications of rules and bring attention to things that might otherwise be overlooked. They also give the rationale behind certain rules, so that the reader understands why the rule must be followed. Any text that appears without heading should be considered as description of the specification.

1.5 Other Applicable Standards

This standard builds on a number of other published open standards.

The LXI Standard does not reproduce those standards, where they are referred to implementation is assumed to be in accordance with the relevant sections of the current version of those standards.

**Software, Protocol Standards**

<table>
<thead>
<tr>
<th>Standard / Protocol Standards</th>
<th>Website</th>
</tr>
</thead>
<tbody>
<tr>
<td>IEEE 802.3 Ethernet Standards</td>
<td><a href="http://www.ieee.org">www.ieee.org</a></td>
</tr>
<tr>
<td>IPV4, IPV6 TCP/IP Protocol</td>
<td><a href="http://www.ipv6.org">www.ipv6.org</a></td>
</tr>
<tr>
<td>VXI-11 Protocol</td>
<td><a href="http://www.vxibus.org/specs.html">www.vxibus.org/specs.html</a></td>
</tr>
<tr>
<td>IVI Driver Standard</td>
<td><a href="http://www.ivifoundation.org">www.ivifoundation.org</a></td>
</tr>
<tr>
<td>Ethernet over WLAN</td>
<td><a href="http://www.ieee.org">www.ieee.org</a></td>
</tr>
<tr>
<td>IEEE 1588 Precision Clock Synchronization for Network Measurements and Control Systems</td>
<td><a href="http://www.ieee.org">www.ieee.org</a></td>
</tr>
<tr>
<td>VISA</td>
<td><a href="http://www.ivifoundation.org">www.ivifoundation.org</a></td>
</tr>
<tr>
<td>IETF RFCs</td>
<td><a href="http://www.ietf.org/rfc.html">www.ietf.org/rfc.html</a></td>
</tr>
</tbody>
</table>

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### Hardware Standards

<table>
<thead>
<tr>
<th>Standard</th>
<th>Description</th>
<th>Location</th>
</tr>
</thead>
<tbody>
<tr>
<td>IEC 60297-1</td>
<td>Rack Dimensions</td>
<td><a href="http://www.iec.ch">www.iec.ch</a></td>
</tr>
<tr>
<td>IEC 297-x</td>
<td>Dimensions of racks, panels</td>
<td><a href="http://www.iec.ch">www.iec.ch</a></td>
</tr>
<tr>
<td>IEEE 802.3af</td>
<td>Power over Ethernet</td>
<td><a href="http://www.iee.org">www.iee.org</a></td>
</tr>
<tr>
<td>TIA/EIA-899</td>
<td>M-LVDS</td>
<td><a href="http://www.tiaonline.org">www.tiaonline.org</a></td>
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</table>

### Environmental and Safety Standards

<table>
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<th>Standard</th>
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<tr>
<td>IEC 61010-1</td>
<td>Safety Requirements</td>
<td><a href="http://www.iec.ch">www.iec.ch</a></td>
</tr>
<tr>
<td>IEC 61326-1-1998</td>
<td>EMC requirements T&amp;M Equipment</td>
<td><a href="http://www.iec.ch">www.iec.ch</a></td>
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<tr>
<td>IEC 60068-1</td>
<td>Environmental testing</td>
<td><a href="http://www.iec.ch">www.iec.ch</a></td>
</tr>
</tbody>
</table>

### Other Applicable LXI Documents

The following documents are available that provide additional information:

<table>
<thead>
<tr>
<th>Document</th>
<th>Location</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>IVI-3.15: IVIxiSync Specification</td>
<td><a href="http://www.ivifoundation.org">www.ivifoundation.org</a></td>
<td>Describes the API required by Class A and Class B LXI Devices</td>
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<tr>
<td>LXI Trigger Bus cable and Terminator Specifications</td>
<td><a href="http://www.lxistandard.org">www.lxistandard.org</a></td>
<td>Describes the specification that have to be met for wired trigger bus cables and terminators to be LXI conformant</td>
</tr>
<tr>
<td>LXI Conformance Spreadsheet</td>
<td><a href="http://www.lxistandard.org">www.lxistandard.org</a></td>
<td>Spread sheet for entering the test results from Plug Fests (or other approved test method) required for LXI Device conformance applications</td>
</tr>
<tr>
<td>LXI Conformance Documentation Template</td>
<td><a href="http://www.lxistandard.org">www.lxistandard.org</a></td>
<td>Document used to enter basic information about an LXI Device in support of a conformance application</td>
</tr>
<tr>
<td>White Papers</td>
<td><a href="http://www.lxistandard.org">www.lxistandard.org</a></td>
<td>White papers concerning the LXI Standard will appear on the consortium website that may provide supporting examples and background information for the LXI Standard</td>
</tr>
<tr>
<td>Licensing Agreement</td>
<td><a href="http://www.lxistandard.org">www.lxistandard.org</a></td>
<td></td>
</tr>
<tr>
<td>LXI Logo</td>
<td><a href="http://www.lxistandard.org">www.lxistandard.org</a></td>
<td></td>
</tr>
</tbody>
</table>
1.6 LXI Website

The LXI Consortium operates a website, www.lxistandard.org, for the standardization activities and promotion of the LXI Standard.

The website includes the latest version of the standard released by the LXI Consortium as a publicly available document.

New versions of the standard being defined, created or edited are available to LXI Consortium members from the “members only” part of the website. Access to this part of the site requires individuals to be associated with member companies of the LXI Consortium and to register with the site.

1.7 LXI Logo and conformance

The LXI Consortium licenses a registered name and logo for use in association with products that are conformant with the standard. In addition, the use of the logo requires that the manufacturer or distributor for the product is either a member of the LXI Consortium, or has agreed to pay any license fees that the LXI Consortium may choose to levy on the use of the logo. Use of the logo also requires the signing of the LXI Consortium IPR policy.

Details of the logo and the Trademark License Agreement can be obtained from the LXI Consortium’s website.

In order to carry an LXI logo and claim LXI conformance, the manufacturer identified on the device front or rear panel must comply with the LXI Consortium’s rules on usage. The product must be conformant with the RULES that apply to its Functional Class – the RULES being those contained in this standard. (See 1.10.1 for a description of Functional Classes). LXI Devices must be conformance tested in a way approved by the LXI Consortium.

1.8 Plug Fest

Manufacturers are required to demonstrate the conformance of LXI devices by attending Plug Fest (or approved alternative) events organized by the LXI Consortium where they demonstrate product conformance through interaction with other LXI Devices. The Plug Fest is used to verify the accuracy of the standard and to ensure that manufacturers differing interpretations are kept to a minimum. Where Plug Fest testing highlights implementation issues or differences in interpretation the standard may be revised to expand, modify, or clarify the standard. Provision is made for manufacturers to declare conformance of other products that share the same generic interface by submitting supporting information to the consortium.

The LXI Consortium will maintain a register of conformant products and will operate a “grievance procedure” to resolve conformance issues raised by users that are not resolved by manufacturers. The Plug Fest also enables manufacturers to exchange views and information that will benefit users under rules of conduct that ensure a collaborative environment for testing.

1.9 Technical Overview

The LXI Standard defines devices using open-standard LAN (Ethernet) for system inter-device communication. The standard will evolve to take advantage of current and future LAN capabilities. It provides capabilities that go well beyond the capability of other test and measurement connectivity solutions. It will provide users with solutions that are denser, smaller, faster, and cheaper than other solutions.
The LXI Standard has three key functional attributes:

A standardized LAN interface that provides a framework for web based interfacing and programmatic control. The LAN interface can include wireless connectivity as well as physically connected interfaces. The interface supports peer to peer operation as well as master slave operation. The LAN can also support the exchange of trigger signals.

A trigger facility based on IEEE 1588 that enables modules to have a sense of time that allows modules to time stamp actions and initiate triggered events over the LAN interface.

A physical wired trigger system based on an M-LVDS electrical interface that allows modules to be connected together by a wired interface using twisted pair transmission lines.

The LXI Consortium expects LXI devices to be used in a wide variety of systems, often including devices that are not by themselves LXI conformant. These devices are likely to include GPIB, PXI, VXI, and LAN instruments with conversion devices, where required. The programming environment for such systems may be more complex, since each may present a different programming environment in order to comply with those standards.

Trigger events over the LAN can be sent by UDP to minimize the latency that can be experienced with TCP/IP protocols. The wired trigger and IEEE 1588 triggers are designed to address concerns about the latency that is sometimes seen in LAN-based systems. The defined wired trigger facility provides a triggered mode of operation that emulates trigger signals based on direct point-to-point connection between instruments. The IEEE 1588 facility allows triggers to be scheduled or events to be time stamped against a system clock.

LXI devices include web pages that can be opened with a web browser to view and change various parameters.
1.9.1 LXI Functional Class Models

The LXI Standard defines three functional classes of instrument.

Functional Class C

These LXI Devices provide a standardized LAN and web browser interface that is conformant with the LXI Standard. These devices do not need to support either the wired trigger or the IEEE 1588 timing aspects. This class is particularly suited to applications where non-LXI products have been adapted to the standard, but it is also well suited to applications where there is no necessity to offer triggered or timed functionality. This class may also include physically small products, such as sensors, that use battery power or PoE (Power over Ethernet) where simple device architecture, low cost and small size are key attributes.

Functional Class B

These LXI Devices provide a standardized LAN interface, synchronization API and supports the IEEE 1588 timing aspects. The IEEE 1588 interface allows devices to execute triggered functions equivalent to those available over GPIB and with similar or better timing accuracy.
**Functional Class A**

These LXI Devices provide a standardized LAN interface, synchronization API, IEEE 1588 operation and a wired trigger bus interface. The wired trigger bus provides a standardized capability of supporting trigger events between devices whose timing accuracy is limited by the physical limitations of cables and LXI Device hardware. The trigger functionality is broadly equivalent to the backplane triggers of modular instruments in card cages, though cable lengths may typically be longer than backplane trigger lengths.

The standard does permit the wired trigger facility to be present on Class C devices.

The Functional classes do not imply any particular physical size for a LXI device.

**1.9.2 RULE – Functional Class**

Manufacturers of LXI devices must clearly declare the Functional Class that a device is conformant with in the data sheet and supporting documentation.

**1.9.3 RULE – Functional Class Declaration**

The functional class declared on the web interface is the definitive source for Functional Class information for an LXI device.

**1.9.4 LXI Physical Overview**

LXI Devices are designed to provide a dense and compact solution for test systems. Many LXI Devices will provide only minimal manual user interfaces to reduce device complexity and space. Many modules are expected to be 1U to 4U high and half rack width, but devices occupying a full rack width are expected to be implemented for many applications, particularly more complex functions. Some LXI Devices, such as sensors, may be much smaller and have mechanical dimensions not intended for rack mounting.

Where devices are intended for rack mounting and are designed to occupy a half rack width, the standard includes recommendations and rules that encourage a “good neighbor” approach to minimize interaction between modules.

The physical standard includes a definition of a recommended half rack width mechanical standard resulting in a LXI Device described as an LXI Unit. The LXI Unit will allow modules to be mounted side by side, irrespective of which vendor is selected, and permit the easy installation of devices with differing heights alongside each other. A mechanical structure is being defined that supports and locates the LXI Units and blocks airflow between adjacent modules. Dimensional information is supplied to ensure conformance to the LXI Unit standard for both the LXI Units and the mechanical structure that supports them. Where forced air cooling is required then LXI Units should provide air intakes on the side and an air exhaust to the rear. Devices should tolerate one side air intake being blocked by a neighbor to ensure that they do not have thermal compatibility problems.

Manufacturers can choose to use alternative half rack width approaches based on proprietary mechanical standards and interconnections systems, an approach most likely to be used for legacy case designs.
User connections to LXI devices are recommended to be on the front while LAN, trigger and power supply connections are on the rear. Indicator lights on the front panel show the presence of power and have a LAN status indicator to ensure users can quickly spot simple functional or connectivity problems. A recommended indicator shows the operating condition of the IEEE 1588 clock system.

The module power can be provided by a standard AC power supply with automatic voltage selection or by a DC power source. Alternatively an isolated DC input connection can be provided (or a Power Over Ethernet source for lower power modules) for applications where AC supply operation is not desirable. A 48 V isolated supply is recommended for these applications to align with the standards for Power Over Ethernet, but other voltages are permitted.

LXI devices are expected to conform to the relevant standards applicable for intended markets for safety and environmental standards.

1.9.5 Reset Mechanism

LXI devices must include a hardware reset mechanism for the LAN (LAN Configuration Initialize). The reset can be implemented as a separate button for devices without a manual user interface or can be provided by a sequence of key presses.

The LAN reset provides a mechanism for recovering from incorrectly configured LAN settings that could render the device incapable of communicating over the LAN. Initiating the LAN reset restores the manufacturers default settings and passwords – settings that must be disclosed in the supporting documentation.

1.9.6 LXI LAN and Web Overview

The LAN interface for LXI devices is intended to use 100baseT or better connections based on the IEEE 802.3 standards. Modules should be designed for fast boot times under all conditions (including circumstances where the network is disconnected). Network speed and duplex settings are automatically detected to simplify system integration.

The LAN interface is defined to minimize the amount of user intervention required for configuring the TCP/IP parameters and the standard ensures that instruments can be quickly ported from one system to another without risk of system hang-ups and a minimal amount of user intervention.

Access to the instrument functions is via a web browser that provides essential information (such as instrument type, serial number, Functional Class) and about key settings on the device Welcome page. The web interface is required to provide an additional IP Configuration page and a Synchronization page (if the device is Functional Class A or B). The Synchronization page includes information about the IEEE 1588 parameters. If the LXI Device allows the user to change any of the instrument settings these have to be password protected. Examples of sample web pages are available to show how the web pages might be presented by an LXI Device.

LXI Devices can be assigned aliases to make it easier for users to identify, particularly for circumstances where more than one of the same type of module may be in the system.

The LXI Standard allows modules to have automatic lookup for the latest firmware or software through a homepage created by the module manufacturer.

The LAN connection is made to the LXI Device with physical cables and can include a wireless access system in the network to cater for applications where a physical connection is difficult to arrange or technically not desirable. Provision is made for the use optical connection systems in the future.
1.9.7 LXI Trigger Interface Overview

LXI provides three trigger mechanisms, one based on triggering over the LAN, the second based on IEEE 1588 Precision Time Protocol running over the LAN interface, the and the third based on a wired trigger interface (LXI Trigger Bus).

The LXI trigger facilities use the principal of a uniform approach – within the performance limitations of each trigger mechanism trigger functions can be performed by any of the methods and can be connected together. A trigger event on the wired trigger, for example, can initiate a LAN or IEEE 1588 trigger event.

The LAN trigger provides a way of programmatically triggering events through driver commands either from the controller to the LXI device or by message exchange between LXI devices. It is available on all Functional Classes of LXI devices that require trigger operation. This trigger mode is the simplest to implement but has the lowest performance because of the potential latency in the LAN communication.

IEEE 1588 Precision Time Protocol is available on Functional Class A and B devices and provides a way of synchronizing clocks across many LXI devices, giving the system a coherent understanding of time that can be used to set up triggered events based on the system time. IEEE 1588 can either be implemented entirely in software or can be supported by dedicated hardware that provides more accurate timing synchronization. Timing accuracy and uncertainty is dependent on the module and the IEEE 1588 implementation, but can be expected to be in the range of 10’s of microseconds to 10’s of nanoseconds.

Triggers or events (measurements) can be initiated at specified times, or can be generated immediately on receipt of the instruction, though in this case there will be a higher degree of time error because of latency in the control system.

The LXI Trigger Bus available on Functional Class A devices connects LXI Devices by a daisy chain or star configuration transmission line system to provide a more deterministic trigger interface that can be event driven (by a device for example) or timed by IEEE 1588 (generating a trigger into the wired trigger system). The interface is based on an 8 channel Multipoint LVDS (M-LVDS) signaling system that allows LXI devices to be configured as sources and/or receivers of trigger signals. The interface can also be configured to a wired OR configuration, permitting LXI devices to respond to trigger events requiring the detection of an event by any of multiple devices initiates or where the last device to be ready initiates events.

The LXI Trigger Bus has an input and an output connector to allow easy daisy chaining of LXI devices. The last device in a daisy chain must have its output connector terminated in a specified load to ensure that transmission lines are correctly terminated.

The LXI Trigger Bus provides a more deterministic inter-module trigger than IEEE 1588 and more closely emulates the trigger facilities provided on instruments, such as oscilloscopes, connected directly by physical connectors. Trigger Adaptors can be used to translate triggers from the LXI Trigger Bus to other trigger levels, or to convey a trigger command from another trigger system to a LXI Wired Trigger event.

The LXI Trigger Bus can include a Star Hub that supports a number of LXI Device daisy chains from a single buffered hub. LXI devices maybe connected to the Star Hub as a simple star network, or they can be set up as hybrid system of star and daisy chain connections. In addition to extending the capacity of the trigger system, the Star Hub can be used to translate a trigger from one channel to another which permits, for example, a device to send a trigger to other devices (including itself) with equal delay times if they are connected to the Star Hub with equal length cables. Star Hubs
may be internally terminated, forcing them to be placed at the end of a chain, or they can use two
collectors for each port and be placed near the centre of a chain.

The LXI Trigger Bus also permits the exchange of clock signals across one or more of the 8
channels available.

1.9.8 LXI Programmatic Overview

The LXI Standard requires that LXI devices have an IVI driver and requires that the relevant IVI
class definition is used where applicable. LXI devices are permitted to be supplied with other
drivers to allow support of other operating environments. The IVI drivers are required to support
VISA resource names. LXI devices must use VXI-11 to provide a discovery mechanism; other
additional discovery mechanisms are permitted.

The programmatic standard defines the way that trigger functions (LAN, IEEE 1588 or LXI Bus
Trigger) are managed and operated for Class A and B LXI devices. It includes a comprehensive
example state machine and architecture for the trigger functions, though LXI devices may only
implement the parts that are relevant to the functions it can usefully support.

There is a requirement that all representations of time derived from the IEEE 1588 are represented
in a uniform way to ensure consistent interpretation of the information.

The programmatic section of the standard contains a considerable amount of detail on programming
rules and resides in separate documents on the LXI website to help improve the clarity of the
standard.

1.10 Hybrid Systems

The LXI Consortium anticipates that some systems will use both LXI conformant devices and
devices conformant with other standards. These systems are described in the Hybrid System section.

A system that contains both LXI Devices and other non LXI devices accessed through non LXI
interfaces is referred to as an Aggregate System. If a system contains only LXI Devices and non-
LXI devices accessed through an interface that make them conformant and indistinguishable from
native LXI Devices, they are part of a Conformant Hybrid System.

The interface types that provide LXI conformant interfaces are identified as Bridges, Adaptors or
Adaptor Toolkits.

Bridges provide an LXI conformant interface for the Bridge and means to control the devices
connected to it. Although the Bridge is LXI conformant, the devices connected to it are not exposed
through an LXI interface, and it is part of Aggregate System. The Bridge provides a different
mechanism for controlling the devices being adapted.

Adaptors present a complete LXI interface the devices being adapted. The combination of the
Adaptor and Adaptee is completely LXI conformant and provides the full LXI experience – it is
indistinguishable from a native LXI device. It can be part of a Conformant Hybrid System.

An Adaptor Toolkit is hardware and software that provides an adaptor function capable of
presenting an LXI interface for adaptee(s), but it is provided as an incomplete solution. The user
must invest additional effort to make the interface LXI conformant.

The interfaces that make a non-LXI device appear to be LXI conformant can carry the LXI logo
after conformance testing. The adapted products are not permitted to carry the LXI logo since they
are not conformant without the additional adaptor interface.
It is believed that it is not possible for a single physical adaptor interface to expose multiple devices as LXI conformant, but further work is anticipated to be carried out in this area.
2 LXI Physical Specifications

2.1 Introduction

The LXI Physical Specifications define mechanical and electrical standards intended for both rack mount and non-rack mount devices. Although there are international standards for full width rack mounted devices, there are no standards for half-width racked devices and this has led to the emergence of a number of de-facto half-width standards introduced by various manufacturers to fill this need. Since it is envisioned that many LXI devices will be smaller half-width, rack mounted, with minimal or no front panel user interface to reduce device complexity and space, this specification introduces a half-width rack mount standard that can be used for the design of emerging half rack LXI devices, and which will avoid the mechanical interoperability problems previously caused by the absence of a common specification.

It is not the intent of this specification to exclude legacy full width rack mounted equipment or new half-width designs built to companies' de-facto standards. To this end, this specification acknowledges existing IEC Publication 60297. Section 2.2 deals with IEC full width mechanical standards while section 2.3 deals with established de-facto half-width mechanical standards.

Mechanical recommendations for new half-width rack mounted LXI devices, which are hereafter referred to as LXI Units, are established in section 2.4. Section 2.5 addresses mounting specifications between LXI Units and de-facto half-width mechanical standards and section 2.6 establishes the LXI Unit cooling recommendations. Electrical Standards are covered under section 2.7 with Section 2.8 covering status indicators. Section 2.9 makes recommendations for environmental standards, and Section 2.10 outlines future roadmap topics.

2.1.1 General Conformance with the Physical Specifications

Devices can conform to the Physical Specifications in four categories:

- Non-rack mounted devices
- Full width rack mounted devices built to IEC 60297 standards
- Half-width rack mounted devices built to de facto standards
- LXI Units built to the specifications defined in this document

All locations of components, connectors, and switches are defined as viewed when facing the panel being described.

2.1.1.1 RULE – Non-Rack Mounted Devices

Non-rack mounted devices shall conform to the following sections:

- Section 2.7 – Electrical Standards
- Section 2.8 – Electrical Standards – Status Indicators
- Section 2.9 – Environmental Standards

2.1.1.2 RULE – Full Width Rack Mounted Devices

Full width rack mounted devices shall conform to the following sections:

- Section 2.2 – Full Width Rack Mounted Devices
- Section 2.7 – Electrical Standards
2.1.1.3 RULE – Half-Width Rack Mounted Devices Built to De Facto Standards

Half-width rack mounted devices built to de facto standards shall conform to sections:

- Section 2.3 – De Facto Half-Width Mechanical Standards
- Section 2.7 – Electrical Standards
- Section 2.8 – Electrical Standards – Status Indicators
- Section 2.9 – Environmental Standards

2.1.1.4 RULE – LXI Unit Half-Width Rack Mounted Devices

Half-width rack mounted LXI Units shall conform to the following sections:

- Section 2.4 – LXI Unit Half-Width Mechanical Standards
- Section 2.5 – LXI Unit Mounting Specifications
- Section 2.6 – LXI Unit Cooling
- Section 2.7 – Electrical Standards
- Section 2.8 – Electrical Standards – Status Indicators
- Section 2.9 – Environmental Standards

2.2 IEC Full Width Mechanical Standards

2.2.1 General Specifications

2.2.1.1 RULE – Conformance to IEC Standards

Full width devices shall conform to existing IEC rack standards in accordance with the relevant sections of whichever version of those standards was current when the device was designed.

2.3 De Facto Half-Width Mechanical Standards

While no official standards exist for half-width rack instruments, vendors have provided instruments in these form factors for several years with significant worldwide installed bases. As a result, de facto standards have been established with system integrators and customers successfully utilizing these instruments in rack based environments.

2.3.1 General Recommendations

2.3.1.1 Recommendation – Half-Width 2U or Higher Dimensions

Half width devices built to de facto standards should conform to the basic dimensions outlined in the IEC standards defined in Section 2.2, and should be capable of being mounted in full width racks when provided with the necessary adaptor kits.
2.3.1.2 Recommendation – LXI Unit Mechanical Interoperability

Manufacturers of half-width devices built to de facto standards are encouraged to develop adaptor kits, as required, that will allow integration with the LXI Unit design specified in Section 2.4, and participate in the definition of the interoperability standards defined in Section 2.5.

2.3.1.3 Recommendation – LXI Unit Thermal Interoperability

Manufacturers of half-width devices built to de facto standards are encouraged to meet the intent of the thermal interoperability standards Section 2.6.

2.4 LXI Unit Half-Width Mechanical Standards

2.4.1 Mechanical Specifications

The following graphic is a representation of a rack mountable, half-width LXI Unit. The key dimensions are shown in the table below.
2.4.1.1 Recommendation: LXI Unit – Maximum Half-Width Device Dimensions

The recommended maximum dimensions for LXI Units are outlined in the following table.

<table>
<thead>
<tr>
<th></th>
<th>1RU High</th>
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<th>3RU High</th>
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<tr>
<td>See permission below</td>
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<td></td>
<td></td>
</tr>
</tbody>
</table>

[1] Minimum recess for varied rail kit thicknesses of upper racked device

2.4.1.1.1 Permission – Height

Some LXI devices may require utilization of the full IEC height (e.g. 1U is 44.45mm, 1.75inches). This is permitted if the device does not encroach or interfere with its immediate neighbors, above or below, when placed in a rack.

2.4.1.1.2 Permission – Narrower Width Bezel or Body

The width of the device bezel or body may be narrower than the maximum recommended.

2.4.1.1.3 Permission – Bezel Width Larger than Width Body

The maximum recommended bezel width may be larger than the width of the device body.
2.4.1.4 Permission – Shorter Depth Bezel
The depth of the bezel may be less than the recommended maximum.

2.4.1.5 Permission – Recess Bottom Rail
The recess bottom rail dimension may be less than the recommended maximum.

2.5 LXI Unit Mounting Specifications

2.5.1 Recommendation – LXI Unit to LXI Unit Adaptors
It is the instrument vendor’s responsibility to provide LXI Unit to LXI Unit adaptors.

2.5.2 Recommendation – LXI Unit to Half-Rack Legacy Device Adaptors
It is the instrument vendor’s or system integrator’s responsibility to provide LXI Unit to half-rack legacy device adaptors.

2.6 LXI Unit Cooling Specifications

2.6.1 RULE – Self-cooling
LXI Units shall be designed to provide their own cooling.

2.6.2 Recommendation – Intake/Exhaust
All cooling air should enter from the sides of the devices and exhaust to the rear.

2.6.2.1 Permission – Intake/Exhaust
Cooling air may enter from the front of the device and exhaust to the rear.

2.7 Electrical Standards
The Electrical Standards define the type and location of all electrical power standards, connectors, switches, indicators, and related components. The following rules shall guide the electrical design and characteristics of LXI devices.

2.7.1 Safety

2.7.1.1 Recommendation – Safety conformance
LXI devices should specify safety conformance to standards appropriate to the intended market (CSA, EN, UL, and IEC).

2.7.2 Electromagnetic Compatibility

2.7.2.1 RULE – Individual Device Shielding
Each LXI device shall provide its own shielding from emitted radiation.
2.7.2.2 Recommendation - EMC Conformance

LXI devices should conform to standards appropriate to the intended market, e.g. FCC, VDE, or MIL Spec. for far field radiated emissions.

2.7.2.3 Recommendation – Conducted Emissions

LXI devices should conform to the standards appropriate to the intended market.

2.7.2.4 Recommendation – EMI Susceptibility

LXI devices should conform to the standards appropriate to the intended market.

2.7.3 Input Power

It is intended that LXI devices are primarily powered by single phase 100-240 volt AC power. However, permissions are granted for the operation from DC power, PoE, or AC power of varying voltage, number of phases, and frequencies to allow for application in specific markets.

2.7.3.1 Recommendation – Universal AC Power

It is recommended that LXI devices be capable of operating autonomously from a single phase input of 100 to 240 VAC (RMS) +/- 10%, at frequencies from 47 to 66 Hz.

2.7.3.1.1 Permission – Non-auto switching AC Power

LXI devices may operate from a single voltage to allow legacy devices with non-auto switching power supplies to be accommodated within the specification.

2.7.3.1.2 Permission – DC or POE Power

LXI devices may operate from DC power either as a direct input or by POE.

2.7.3.2 Recommendation: DC Power

If DC power is utilized, it should be an isolated 48VDC input.

2.7.3.2.1 Permission: Two and Three Phase Power

LXI devices may operate from two and three phase power.

2.7.3.2.2 Permission: Other Line Frequencies

Other power line frequencies beyond 47 Hz to 66 Hz are permitted to allow for specific application environments.

2.7.4 Power Switch

A power switch is optional.

2.7.4.1 Recommendation – Power Switch Location

The power switch is optional, but when implemented it should be located in the lower right hand corner of the rear panel.
2.7.4.2 Permission – Front Panel Power Switch Location

Front panel power switch location is permitted.

2.7.5 LAN Configuration Initialize (LCI)

2.7.5.1 RULE – LCI Mechanism

LXI devices shall provide an LCI Mechanism that, when activated, places its network settings to a default state. The functions performed by this mechanism are defined in Section 8.14.

2.7.5.2 RULE – LXI Devices Without a Front-Panel Manual Data-Entry Method

Devices shall provide an LCI mechanism by either:

a) A separate recessed mechanical LCI mechanism on the rear or front of the device (rear is preferred).

b) A soft LCI mechanism through a permanently attached user interface (e.g., a front panel, monitor, mouse, keyboard, et cetera) that does not use the LXI bus (LAN) as the interface.

2.7.5.2.1 Recommendation – Not Using LCI Mechanism for Other Purposes

The mechanism (especially that described in RULE 2.7.5.2) that invokes the LAN Configuration Initialization should not be used for any other function.

If this mechanism is also used for something else, such as instrument reset, however, the two ways to actuate it should be distinct enough so that it is difficult for a user to invoke the wrong one, and the mechanism (e.g., “LAN RESET” Button) should be labeled to show it performs multiple functions.

2.7.5.3 RULE – LCI Mechanism Protection

The LCI Mechanism shall be protected by a time-delay, user query, or mechanical protection feature designed to prevent inadvertent operation.

2.7.5.4 Recommendation – LCI Mechanism Location

The LCI should be located on the rear panel of the device in the same general area as the power switch, if present.

2.7.5.4.1 Permission – LCI Mechanism Location

To address market specific requirements, the LCI may be located on the front panel of the device.

2.7.5.5 Recommendation – LCI Mechanism Label

The LCI Mechanism should be labeled “LAN RST” or “LAN RESET”.

2.7.5.5.1 Permission – LXI devices with a Front Panel

For devices with a front-panel manual data-entry method such as a keypad or touch panel user interface, the LCI functions may be executed by a single keystroke or a sequence of keystrokes.
2.7.5.5.2 Permission – LCI Mechanism Lockout

For LXI devices intended for deployment in critical conditions, manufacturers can include an LCI Mechanism Lockout function in the form of a protected or internal switch or jumper that prevents all reset functions from being accessed.

2.7.6 Power Cords and Connectors

2.7.6.1 RULE – Rear Panel Power Connector

The AC or DC power connector shall be located on the rear panel.

2.7.6.2 Recommendation – Power Connector Location

It is recommended that the power connector be located on the right hand side of the rear panel.

2.7.6.3 Recommendation – AC Power Connector Type

It is recommended that LXI devices operating from a single phase AC input, as recommended in Section 2.7.3, utilize an IEC 320 type connector.

Multi-phase AC input devices should use an AC input connector compliant with the safety and EMC standards applicable to the device.

2.7.7 Fusing or Over-Current Protection Device

2.7.7.1 Recommendation – Over-Current Protection

If a fuse or over-current protection device is required, it should be integral to or located adjacent to the input power connector.

2.7.8 Grounding

2.7.8.1 Recommendation – Unit Grounding

LXI devices should conform to standards appropriate to the intended market.

2.7.9 LAN Connectors

This section deals with physical IEEE 802.3 LAN connectors. While wireless IEEE 802.11 wireless LAN devices are permitted, this specification makes no recommendations related to the necessary transmitter/receiver.

2.7.9.1 RULE – IEEE 802.3

Physical Ethernet connections shall be IEEE 802.3 compliant.

2.7.9.2 Recommendation – LAN Connector Location

The LAN connector should be on the rear panel of the device at the right hand side as constrained by the location of the other connectors.
2.7.9.3 Recommendation – RJ-45 Connector
RJ-45 connectors should be used unless technical reasons require otherwise.

2.7.9.4 Recommendation – M12 Style Connectors
If RJ-45 style connectors are not acceptable, M12 style connectors should be considered.

2.7.9.5 Recommendation – Non-Sealed Connections
For applications that do not require sealed connections, the following specifications should apply for the RJ-45 connections:

- Electrical: ANSI/TIA/EIA-568-B.2 (Category 5E), either shielded or unshielded
- Mechanical: IEC 60603-7

2.7.9.6 Recommendation – Sealed Connections
For applications that require sealed RJ-45 connectors, those connectors should adhere to the ODVA specification for sealed RJ-45 jacks.

2.7.9.7 Recommendation – Shielded CAT 5 cable
Shielded CAT 5 cable should be used for devices installed in harsh environments requiring additional electrical or mechanical protection.

2.7.9.8 Recommendation – Integrated Magnetics
LXI devices should utilize shielded modular jacks with integrated magnetics.

2.7.10 LXI Trigger Bus Connectors

2.7.10.1 RULE – Trigger Bus Connectors
For devices incorporating the LXI Trigger Bus, the number and type of Trigger Bus connectors shall be as specified in Section 5.

2.7.10.2 Recommendation – Connector Location
Location of the Trigger Bus connectors should be on the rear panel of the device at the right hand edge as constrained by the location of the power connector.

2.7.10.3 Recommendation – Connector Orientation
The LXI Trigger Bus connectors should be vertically stacked with a minimum vertical, center to center, separation of 11.05mm (0.435 inches).

2.7.10.4 Permission – Connector Orientation
The LXI Trigger Bus connectors may be horizontally mounted immediately next to each other.
2.7.10.5 Permission – Vendor-Specific Triggers

Vendor-specific hardware trigger interfaces are permitted.

2.7.11 Signal I/O Connectivity Interfaces

2.7.11.1 Recommendation – Signal Connections

Signal connections should be located on the front panel of the device.

2.7.11.1.1 Permission – Signal Connections

Signal connections are permitted on the rear panel of the device. This allows vendors to align with selected market segment and customer requirements.
## 2.8 Electrical Standards – Status Indicators

LXI devices have LED status indicators for Power, LAN, and, where applicable, IEEE 1588.

The following table summarizes the recommendations for the color, location, orientation, and labeling of the status indicators:

<table>
<thead>
<tr>
<th></th>
<th>Power Indicator</th>
<th>LAN Status Indicator</th>
<th>IEEE 1588 Clock Status Indicator</th>
</tr>
</thead>
<tbody>
<tr>
<td>LED Color(s)</td>
<td>Bi-Color (Orange/Green)</td>
<td>Bi-Color (Red/Green)</td>
<td>Bi-Color (Red/Green)</td>
</tr>
<tr>
<td>Front panel location</td>
<td>Lower left hand corner of the front panel Power</td>
<td>Next to and to the right of the Power Indicator Power</td>
<td>Next to and to the right of the LAN Status Indicator Power LAN 1588</td>
</tr>
<tr>
<td>Horizontal Orientation</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Note: The status indicators are ordered in the LXI device turn-on sequence.</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Power LAN 1588 (Left LED)</td>
<td>Power LAN 1588 (Middle LED)</td>
<td>Power LAN 1588 (Right LED)</td>
</tr>
<tr>
<td></td>
<td>LXI device turn-on sequence: First, enable power.</td>
<td>LXI device turn-on sequence: Second, acquire LAN IP Configuration.</td>
<td>LXI device turn-on sequence: Third, acquire 1588 clock.</td>
</tr>
<tr>
<td>Vertical Orientation</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Note: The status indicators are ordered in the LXI device turn-on sequence.</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1588</td>
<td>1588</td>
<td>1588</td>
</tr>
<tr>
<td></td>
<td>LAN Power</td>
<td>LAN Power</td>
<td>LAN Power</td>
</tr>
<tr>
<td></td>
<td>(Bottom LED)</td>
<td>(Middle LED)</td>
<td>(Top LED)</td>
</tr>
<tr>
<td></td>
<td>LXI device turn-on sequence: First, enable power.</td>
<td>LXI device turn-on sequence: Second, acquire LAN IP Configuration.</td>
<td>LXI device turn-on sequence: Third, acquire 1588 clock.</td>
</tr>
<tr>
<td>Labeling [1]</td>
<td>Universal power symbol, or PWR, or POWER</td>
<td>LAN</td>
<td>1588</td>
</tr>
</tbody>
</table>

[1] The location of labels is not specified. They are left to the discretion of each vendor.
2.8.1 Power Indicator

2.8.1.1 RULE – Power Indicator

A Power Indicator shall be provided on the front panel of the device.

2.8.1.2 Recommendation – Power Indicator Color

Some LXI devices may keep the power supply in stand-by mode while the device itself is turned off. From a safety perspective, it is recommended this state be identified by the power status indicator.

For LXI devices that utilize a Standby Power state, the Power indicator should be a tri-state bi-color (Orange/Green) LED whose states are identified as follows:

<table>
<thead>
<tr>
<th>State</th>
<th>Status</th>
<th>Interpretation</th>
</tr>
</thead>
<tbody>
<tr>
<td>OFF</td>
<td>No Power</td>
<td>No power is applied.</td>
</tr>
<tr>
<td>STANDBY</td>
<td>Standby Power</td>
<td>The Standby state is used for safety purposes by those devices that keep the power supply hot while the device itself is turned off.</td>
</tr>
<tr>
<td>ON</td>
<td>Power is ON</td>
<td>Power is applied.</td>
</tr>
</tbody>
</table>

For LXI devices that do not utilize a Standby Power state, the Power indicator should be a single color (Green) LED whose states are identified as follows:

<table>
<thead>
<tr>
<th>State</th>
<th>Status</th>
<th>Interpretation</th>
</tr>
</thead>
<tbody>
<tr>
<td>OFF</td>
<td>No Power</td>
<td>No power is applied.</td>
</tr>
<tr>
<td>ON</td>
<td>Power is ON</td>
<td>Power is applied.</td>
</tr>
</tbody>
</table>
2.8.1.3 Recommendation – Power Indicator Location

The Power Indicator should be placed on the lower left hand corner of the device.

2.8.1.4 Recommendation – Power Indicator Orientation

The Status Indicators should be horizontally oriented as follows.

From left to right: Power Indicator, then LAN Indicator, then 1588 indicator.

2.8.1.4.1 Permission – Power Indicator Orientation

It is permitted for the Status Indicators to be vertically oriented as follows.

From bottom to top: Power Indicator, then LAN Indicator, then 1588 indicator.

2.8.1.4.2 Permission – Power Indication for Devices with a Front Panel

For devices with a front panel, the equivalent Power Indicator may be presented in a manner consistent with the design and capabilities of the front panel, such as a marked switch or an integrated display.

The use of symbols on a display, instead of LED status indicators, is permitted. Such indicators do not have to be permanently visible but could be accessed via some display navigation method.

2.8.1.5 Recommendation – Power Indicator Label

The Power Indicator should be labeled with either the Universal Power Symbol, PWR, or POWER.

2.8.2 LAN Status Indicator

The LAN Status Indicator fulfills different functions from the standard LAN activity indicator often built into RJ-45 LAN connectors. The LAN status indicator should be a bi-color (Red/Green) LED providing two functions: LAN fault indication and device identification.

2.8.2.1 RULE – LAN Status Indicator

A LAN Status Indicator shall be provided on the device front panel.
2.8.2.2 Recommendation – LAN Status Indicator Color and States

The LAN Status Indicator should be a bi-color (Red/Green) LED whose states are identified as follows:

<table>
<thead>
<tr>
<th>State</th>
<th>Status</th>
<th>Interpretation</th>
</tr>
</thead>
<tbody>
<tr>
<td>On – Solid green, steady illumination</td>
<td>Normal Operation</td>
<td>Normal Operation</td>
</tr>
<tr>
<td>On – Flashing Green</td>
<td>Device Identify</td>
<td>A Device Identification command was activated on the device’s web pages or driver interface. The status indicator shall continue to flash green until told to do otherwise (this is not a one time flash, rather it is toggled on and off by a web interface control)</td>
</tr>
<tr>
<td>On - Solid Red, steady illumination</td>
<td>LAN Fault</td>
<td>See section 8.11 for LAN Fault Conditions</td>
</tr>
</tbody>
</table>

2.8.2.2.1 Permission – LAN Status Indicator Color and States

If an LXI device’s design precludes the use of a bi-color LED, the use of a single Green colored LED is permitted. In this situation, the LAN status states should be interpreted as follows:

<table>
<thead>
<tr>
<th>State</th>
<th>Status</th>
<th>Interpretation</th>
</tr>
</thead>
<tbody>
<tr>
<td>On – Solid green, steady illumination</td>
<td>Normal Operation</td>
<td>Normal Operation</td>
</tr>
<tr>
<td>On – Flashing Green</td>
<td>Device Identify</td>
<td>A Device Identification command was received over the LAN. The status indicator shall continue to flash green until told to do otherwise (this is not a one time flash, rather it is toggled on and off by a web interface control)</td>
</tr>
<tr>
<td>Off</td>
<td>LAN Fault</td>
<td>See section 8.11 for LAN Fault Conditions</td>
</tr>
</tbody>
</table>

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2.8.2.3 Recommendation – LAN Status Indicator Location

The LAN Status Indicator should be placed on the lower left hand corner of the front panel, next to and to the right of the Power Indicator.

2.8.2.3.1 Permission – LAN Status Indicator Location

If an LXI device’s design precludes placing the LAN Status Indicator in the recommended front panel location, it may be placed on the rear panel.

2.8.2.4 Recommendation – LAN Status Indicator Orientation

The Status Indicators should be horizontally oriented as follows.

From left to right: Power Indicator, then LAN Indicator, then 1588 indicator.

2.8.2.4.1 Permission – LAN Status Indicator Orientation

It is permitted for the Status Indicators to be vertically oriented as follows. From bottom to top: Power Indicator, then LAN Indicator, then 1588 indicator.

2.8.2.4.2 Permission – LXI devices with a front panel display

For devices with a front panel display the equivalent indications may be presented in a different manner consistent with the design and capabilities of the front panel.

The use of symbols on a display, instead of LED status indicators, is permitted. Such indicators do not have to be permanently visible but could be accessed via some display navigation method.

2.8.2.4.3 Permission – Devices with limited or no front panel display

For devices with front panels with limited capabilities, or which are difficult or impossible to reconfigure, or devices with no front panel displays, the equivalent indications may be presented in a different manner consistent with the design and capabilities of the front panel. Recommended symbol bitmaps are provided by the LXI Consortium and are available on the LXI website. They may be re-sized as required for specific display resolutions.

2.8.2.5 Recommendation – LAN Status Indicator Label

The LAN Status Indicator should be labeled as LAN.

2.8.3 IEEE 1588 Clock Status Indicator

The IEEE 1588 Clock Status Indicator is designed to show both the status and the type of clock in the device. It is a multi-state device, in that it can flash at two different rates, and provide a steady or no indication depending on the type and status of the clock present.

2.8.3.1 Recommendation – IEEE 1588 Clock Status Indicator

An IEEE 1588 Clock Status Indicator should be provided on the front panel of the device.
2.8.3.2 Recommendation – IEEE 1588 Clock Status Color

The IEEE 1588 Clock Status Indicator should be a single, bi-color LED (Red/Green) whose states are identified as follows:

<table>
<thead>
<tr>
<th>State</th>
<th>PTP State of Port</th>
</tr>
</thead>
<tbody>
<tr>
<td>Off</td>
<td>Not Slave, Not Master, and Not Faulty</td>
</tr>
<tr>
<td>On – Solid Green</td>
<td>Slave</td>
</tr>
<tr>
<td>On – Blinking Green once every second</td>
<td>Master but not Grandmaster</td>
</tr>
<tr>
<td>On – Blinking Green once every two seconds</td>
<td>Master and also Grandmaster</td>
</tr>
<tr>
<td>On – Solid Red</td>
<td>Faulty</td>
</tr>
</tbody>
</table>

2.8.3.2.1 Permission – IEEE 1588 Clock Status Color

If an LXI device’s design precludes the use of a bi-color LED, the use of a single Green colored LED is permitted. In this situation, the IEEE 1588 Clock states should be interpreted as follows:

<table>
<thead>
<tr>
<th>State</th>
<th>PTP State of Port</th>
</tr>
</thead>
<tbody>
<tr>
<td>Off</td>
<td>Not Slave and Not Master</td>
</tr>
<tr>
<td>On – Solid Green</td>
<td>Slave</td>
</tr>
<tr>
<td>On – Blinking Green once every second</td>
<td>Master but not Grandmaster</td>
</tr>
<tr>
<td>On – Blinking Green once every two seconds</td>
<td>Master and also Grandmaster</td>
</tr>
</tbody>
</table>

2.8.3.3 Recommendation – IEEE 1588 Clock Status Indicator Location

The IEEE 1588 Clock Status Indicator should be placed on the lower left hand corner of the device, next to and to the right of the LAN Status Indicator.

2.8.3.4 Recommendation – IEEE 1588 Clock Status Indicator Orientation

Orient the Status Indicators horizontally oriented as follows.

From left to right: Power Indicator, then LAN Indicator, then IEEE 1588 indicator.

2.8.3.4.1 Permission – IEEE 1588 Clock Status Indicator Orientation

It is permitted for the Status Indicators to be vertically oriented as follows. From bottom to top: Power Indicator, then LAN Indicator, then IEEE 1588 indicator.
2.8.3.4.2 Permission – LXI devices with a Front Panel Display

For devices with a front panel display the equivalent indications may be presented in a different manner consistent with the design and capabilities of the front panel.

The use of symbols on a display, instead of LED status indicators, is permitted. Such indicators do not have to be permanently visible but could be accessed via some display navigation method.

2.8.3.5 Recommendation – IEEE Clock Status Indicator Label

Label the IEEE Clock Status indicator “1588”.

2.8.3.5.1 Permission - Application Specific Status Indicators

Additional application specific status indicators, beyond the basic ones already outlined, are permitted.

2.9 Environmental Standards

2.9.1 Standards Conformance

Environmental and Safety Standards

<table>
<thead>
<tr>
<th>Standard</th>
<th>Website</th>
</tr>
</thead>
<tbody>
<tr>
<td>IEC 61010-1 Safety Requirements</td>
<td><a href="http://www.iec.ch">www.iec.ch</a></td>
</tr>
<tr>
<td>IEC 61326-1-1998 EMC requirements T&amp;M Equipment</td>
<td><a href="http://www.iec.ch">www.iec.ch</a></td>
</tr>
<tr>
<td>IEC 60068-1 Environmental testing</td>
<td><a href="http://www.iec.ch">www.iec.ch</a></td>
</tr>
<tr>
<td>TIA/EIA-899 M-LVDS</td>
<td><a href="http://www.tiaonline.org">www.tiaonline.org</a></td>
</tr>
</tbody>
</table>

2.10 Future Roadmap Topics

The following topics are designated as future (post-Rev. 1.0) roadmap items for further consideration.

2.10.1 Mechanical

2.10.1.1 LXI Unit and Half-Rack Legacy Device Enclosure

Instrument vendors, system integrators, or customers racking multi-vendor LXI Units and varied height half-rack width legacy devices may wish to develop an enclosure similar to the following picture. This type of “letter-box” enclosure will be defined in a future roadmap revision and will include, but not be limited to, the following aspects:

Engineering drawings for the enclosure.

The dimensions from the front of the IEC rack mounting rail (or reference plane) to the front face of the LXI device when installed with any adapter kit in the letterbox.
The position, depth and thread type for the rear panel mounting points for LXI Units, in a form suitable for use with any integer U height device.

Manufacturer-specific adaptors may be required to allow front stabilization and rear attachment to the letter-box.

2.10.1.2 Market-Specific Portable Carriers

For Rev 1.x, investigate market-specific requirements for LXI device portable carriers.

2.10.2 Card Cage, Enclosed, or Hybrid Solutions

Card cage, enclosed, or hybrid solutions will be evaluated as a roadmap item. This includes, but is not limited to, items like status indicators, power, etc.

2.10.3 Market-Specific Environmental Standards

For Rev 1.x, investigate market-specific environmental standards.
3 LXI Device Synchronization and LAN-Based Triggering

3.1 Introduction

The triggering and synchronization capabilities of an LXI device enable system integrators to:

- Control the sequencing of states within a module or across the system
- Control the timing of issuing and handling of local and system events
- Order or correlate measurement data and significant events based on timestamps

LXI allows three modes of inter-module event communication:

- Via driver commands from a controller (or any other device that can function as a controller) to a module via the LAN
- Direct module-to-module messages via LAN (direct LAN messaging)
- Hardware trigger lines from module to module

LXI allows five modes of triggering:

- Driver command-based: A driver interface on the controlling computer is used to directly transmit a command to a module. (Required for Class A, B and C)
- Direct LAN messaging: A data packet containing triggering information (including a time stamp) is sent directly from one module to another via the LAN. (Required for Class A and B)
- Time-based events: An IEEE 1588-based time trigger is set and executed internally in a module. (Required for Class A and B)
- LXI Trigger Bus-based: A module function is triggered via a voltage on the LXI Trigger Bus (see section 6 of the LXI specification). (Required for Class A only)
- Optional vendor-specific hardware triggers. (Applies to Class A, B and C)

Roadmap: The LXI Consortium will require that LXI Class A and LXI Class B devices conform to IEEE 1588-2008 shortly after that version is approved by the appropriate IEEE governing body. Approval of IEEE-1588 2008 is expected sometime later in 2007. LXI Class A and B devices are currently required to conform to IEEE 1588-2002. The Consortium will modify its specifications and related conformance testing procedures to implement this change as soon as it can reasonably do so given the status of the IEEE 1588 working group’s activities. The LXI Consortium will strive to make the technical information needed by LXI members and licensees to design for conformance with IEEE 1588-2008 available in draft form as early as possible. More information regarding IEEE 1588-2008 is available from the IEEE working group’s website: http://ieee1588.nist.gov.

3.2 RULE – Implementation of IEEE 1588 Precision Time Protocol

Each LXI device that complies with LXI Class A or LXI Class B shall provide fully conformant IEEE Std 1588 Precision Time Protocol (PTP) functionality, as defined by IEEE Std 1588-2002, sub clause 9.2.1.
3.2.1 RULE – IEEE 1588 Slave-Only Mode Disallowed

Each LXI device that includes IEEE 1588 functionality shall include full IEEE 1588 master clock capability. Slave-only node functionality (per IEEE 1588 clause 9.2.2) is not allowed.

3.2.2 Recommendation – Precision of LXI Module Clocks

Each Class A or Class B LXI device should implement IEEE 1588 to a precision adequate for the timing performance of the device. It is further recommended that LXI devices implement time precision of 40 nanoseconds or better.

3.2.2.1 Permission – Software implementation in controllers

Software implementations of IEEE 1588 may be used in controllers but is discouraged in instrument modules.

3.2.3 Recommendation – Use of IEEE 1588 Boundary Clock

The timing precision of a system of LXI devices will be limited by, among other things, the quality of the LAN switch in the system. The use of a LAN switch that includes a stable clock for use in IEEE 1588 synchronization (known as a ‘boundary clock’ in the IEEE 1588 specification) is highly recommended.

3.2.4 Recommendation – Traceability to UTC

The time base of an LXI system should be traceable to UTC.

3.2.5 RULE – Must Be Able to Set UTC Time

Any LXI device implementing IEEE 1588 functionality shall be capable of being made traceable to UTC in the event that it is selected as the grandmaster clock by the IEEE 1588 protocol.

3.2.6 RULE – Must Be Able to Set UTC Time Manually

Traceability to UTC shall be, at a minimum, available by manual setting by an operator (HAND in IEEE 1588).

3.2.6.1 Recommendation – Battery Backup for Clocks

It is recommended that devices capable of being a grandmaster clock provide battery backup time-of-day clocks to provide holdover in the event of power failure.

3.2.6.2 Recommendation – Periodic Check for Time Drift

If the system is using a UTC time base, clocks that are selected as the grandmaster should attempt to do an hourly sanity check against a recognized UTC server. If the difference is greater than a pre-set value, an error message should be transmitted. The recommended value is 10 seconds, although implementers are free to set this value to anything that meets application requirements.
3.2.6.2.1 RULE – Transmission of Time Drift Error

Any error message that is transmitted as a result of 3.2.6.2 shall format the error into a UDP packet, as specified in Section 4.

3.2.7 Recommendation – Include at Least One Highly Stable Clock

All LXI systems should include at least one module specifically designed to provide a very stable UTC time base.

3.2.8 RULE – Communication of Time Must Use IEEE 1588 Time Base

All inter-module time references in an LXI system shall be based on the IEEE 1588 timescale and epoch, using the byte format for time that is specified in the IEEE 1588 spec. Translation between the IEEE 1588 time base and UTC in an LXI device shall only occur at the interface to another subsystem external to the portion of the system operating based wholly or in part on time (e.g. a user interface or a database). All time references between modules or between modules and controllers shall use the IEEE 1588 format and time base. All LXI devices required to make this translation shall use the leap second information available from the IEEE 1588 protocol.

3.2.9 RULE – Controller Capability to Set Time

All LXI controllers shall be capable of setting the IEEE 1588 time in the grandmaster via the HAND option of IEEE 1588.

3.3 RULE – Use of Specified Format for Module-to-Module Communications

All LXI devices that implement IEEE 1588 shall also implement module-to-module communication of time-based events using UDP multicast and TCP point-to-point transports. The data format for these transmissions is defined in Section 4. The programmatic use model for them is defined in Section 6.4.

3.3.1 DEPRECATED RULE – Ignore Duplicate UDP Packet Receptions

*Rule 3.3.1 has been deprecated because the behavior required is impossible to implement in the general case.*

All LXI devices that comply with Rule 3.3 shall have the ability to detect and ignore duplicate UDP packets. (Note: In this context, the term “ignore” means that the system acts as though each packet is received only once, even if it is actually received multiple times. There are no restrictions as to how this rule should be implemented.)

3.3.2 RULE – Mechanism for LAN-based Module-to-Module Communications

All LXI devices that implement IEEE 1588 shall also listen for and respond to unicast TCP and multicast UDP event messages (as defined in Section 4).

The following definitions and figure shall be used to clarify the timing behavior of responses to LAN event messages:
**Reception Time** – Time when a LAN event message is received by a module. This time is normally logged in the event log.

**Trigger Time** – The point in time at which the response to an event begins. This is the timestamp field of the LAN message. In the classic SCPI trigger state machine, the response to a trigger event causes the state machine to begin the optional offset (e.g., trigger delay or advance) and then the triggered action (e.g., take a measurement, enable source output, change switch configuration, etc.).

**T1 = Trigger Time** – Timestamp field of the LAN message. If T1 is zero (“Now”), it is replaced with the current time of the receiving clock.

**Dt = Offset** – This may be zero, positive, or negative.

**T2 = Action Time** – Examples of this time are as follows: take a measurement, enable source output, change switch configuration, etc.

---

Modules shall be configurable as to whether or not they transmit an event message for any given event. Such events shall be the following:
- Events specified in this standard
- Module-specific events specified by the vendor
- Application-specific events specified by the user.

For these event messages:
1. The eventID shall be the following:
   a. A value specified in this document; e.g., LAN0
   b. A vendor-specific value documented by the vendor
   c. An application-specific value specified by the user.
2. The timestamp (T1) in the transmitted event message shall be the time at which the event occurred or will occur with respect to the local clock of the transmitting module. Please refer to Section 3.9 for information on timestamps of zero. Note that it is permissible for a module to schedule a local or system-wide event in the future. For example, a controller can specify that “test-A” will start at some future time, or an instrument can specify that it will go out of calibration at some future time.

3. The data fields shall be null by default, but additionally may be:
   a. Specified in the standard for standard-specified eventIDs
   b. A vendor-specific value documented by the vendor
   c. Application-specific as specified by the user.

It shall be possible for the user to program the module’s response to an event as follows:

1. By default, the module shall not respond to the event message.
2. The nature of the response shall be based on the eventID and shall be specified by the user. This does not preclude vendors from specifying a default response that can be overridden by the user.
3. The action time, T2, shall be computed as T2 = T1 + Dt. The time T2 may be in the past or the future and shall be interpreted in the context of the local clock of the receiver of the event message. By default, Dt shall be zero.
4. The use of the data field shall be specified by the user. This does not preclude vendors from specifying a default

For all events, the specified response shall occur when the time T2 matches the local clock in the receiving module. Note that the accuracy and precision of this match depends on the implementation of IEEE 1588 and the design of the module.

When T2 is in the future, modules shall schedule an internal alarm or similar mechanism to cause the specified response to occur at the proper time.

When T2 is in the past, modules shall take immediate action by default. Additionally, vendors may provide user-selectable options for the behavior when T2 is in the past, including no response (ignore) as well as module-specific semantics (e.g., report data previously measured at time T2 and stored for future retrieval).

While many instruments will likely provide a standard trigger/arm state machine model to respond to event messages, the use of module-to-module communications is not limited to such behavior models. For the trigger layer of the trigger/arm state machine (as seen in the figure accompanying Rule 6.4.4), T1 is the time at which the transition from “WaitingForTrigger” to “Wait: trigger delay” occurs. Dt is the time spent in the “Wait: trigger delay” state and T2 is the time at which the measure layer is entered.

Please refer to Section 6.4 for reserved eventIDs.

The Programmatic Interface section of the LXI specification contains details on the API for these purposes.

3.3.3 Recommendation – Support LAN Messages with Arbitrary Names

In addition to the names "LAN0" through "LAN7", modules should support LAN messages with arbitrary names.
3.3.4 RULE – Ignore Message with No Action or Routing Associated with Its Name

If a module receives a LAN message whose name has no action or routing information associated with it, the module shall ignore the message.

3.3.5 Recommendation – Programmable Modules

Rule 3.3.2 implies that LXI devices can respond to direct module-to-module LAN messages in different ways. There are a number of ways in which this feature can be implemented, the simplest of which is to pre-define all of the possible responses that a module might execute on the receipt of any LAN message. However, it is recommended that modules be programmable for this purpose, and that executable code can be downloaded.

3.3.6 Permission – LXI Class C Module-to-Module Communications

LXI Class C modules, which are not required to include IEEE 1588 functionality, may still participate in the intermodule communication system referred to in Rule 3.3.2 by using a value of zero for the time stamp in all communications. This value will be interpreted to mean “now” by receiving modules.

3.4 RULE – Inclusion of IEEE 1588 Time-Based Triggers

Class A and Class B instruments shall include one or more time-based triggers. This is necessary for implementation of autonomous time-based event coordination in the module.

3.5 RULE – Driver Functionality Must Be Available from Within Modules

For Class A and Class B instruments, any trigger-related function executable via the controller-based driver (e.g., IVI) shall also be executable from within the module. These functions shall be executable by the local module based on both direct module-to-module messaging (e.g., LAN packets from other system modules, as defined in Section 4) and internal time-based events (e.g., IEEE 1588-based time triggers).

3.5.1 Recommendation – Driver Functions Should be Available as Responses

For Class A and Class B instruments, any measurement-related function executable via the controller-based driver (e.g., IVI) should also be executable from within the module. These functions should be executable by the local module based on both direct messaging (e.g., LAN packets from other system modules, as defined in Section 4) and internal time-based events (e.g., IEEE 1588-based time triggers). (Note: The term “measurement-related” does not refer to basic instrument configuration. For instance, setting the frequency of a source is a “measurement-related function”, while setting the IP address of an instrument is not.)

3.5.2 Recommendation – Allow Multiple Actions from a Single Trigger

Modules should provide for the execution of multiple events or configuration changes to be initiated by a single event or time trigger, programmable by the user.
3.6  Recommendation – Trigger Module Functions via any Method

Any internal function that can be triggered should be configurable to be triggered by any and all of the five options: driver command from the controller, direct LAN message from another module, time-based, LXI trigger bus, and optional vendor specific hardware level triggers.

3.6.1 RULE – Specify Trigger Response Times

If Recommendation 3.6 is implemented, for each triggered function that is implemented in a module the published specification shall include the time that it takes to respond to each of the possible triggering methods. This information shall include the minimum, maximum, and typical response times (exclusive of LAN latencies and other timing effects that are external to the module itself). For response times that are probabilistic in nature, the minimum and maximum response times shall be specified with a 95% confidence. If the response time is unknown or cannot be determined, the manufacturers shall explicitly state that the time is unknown. This information shall be available as a part of the documentation that accompanies each LXI device (whether printed or electronic).

3.6.1.1 Recommendation – Trigger Output Response Times Available via Driver

The information provided for in 3.6.1 should be available on the controller via the driver interface.

3.6.2 Recommendation – Trigger Responses Available via Driver Call

Events under Recommendation 3.6 should also be accessible via the driver interface on the controller.

3.6.3 Permission – Exemption of Triggering Requirements for Class B and C

Class B and Class C devices, which have fewer triggering options, are obviously exempted from all requirements to enable triggering for types of triggers that do not exist on the device.

3.7  Recommendation – Trigger Outputs Can Be Transmitted by any Method

Any non-controller device capable of detecting an event that can be used as a trigger should be configurable to communicate this trigger event to other devices by LAN message (using the data packet format defined in Section 4), LXI trigger bus, or optional vendor specific hardware.

3.7.1 RULE – Specify Trigger Output Response Times

If Recommendation 3.7 is implemented, for each event that can cause a trigger the published specification shall include the time it takes the module to respond to the event and transmit a trigger by each of the possible triggering methods. This information shall include the minimum, maximum, and typical response times. For response times that are probabilistic in nature, the minimum and maximum response times shall be specified with a 95% confidence. If the response time is unknown or cannot be determined, the manufacturers shall explicitly state that the time is unknown. This information shall be available as a part of the documentation that accompanies each LXI device (whether printed or electronic).
3.7.1 Recommendation – Trigger Output Response Times Available via Driver

The information provided for in Rule 3.7.1 should be available on the controller via the driver interface.

3.7.2 Recommendation – Events Available via Driver Call

Events under Recommendation 3.7 should also be accessible via the driver interface on the controller.

3.7.3 Permission – Exemption of Triggering Requirements for Class B and C

Class B and Class C devices, which have fewer triggering options, are obviously exempted from all requirements to output triggers for types of triggers that do not exist on the device.

3.8 RULE – Require Specified Data Format for Module-to-Module Communications

Direct module-to-module LAN message based event communication shall use messages with the on-the-LAN format described in Section 4.

3.8.1 RULE – Interpretation of Zero-Valued Time Stamps

Upon encountering a time stamp value of zero, all modules shall interpret the time stamp to mean “now”, i.e., the current time as it is understood by the module.

3.8.2 RULE – Use of LXI "Domain"

All LXI devices that participate in module-to-module communications per Rule 3.8 shall use the "domain" byte in the LAN packet (as specified in Section 4) to ensure that each received data packet is intended for receipt by the device. Each device shall be configurable as to the domain of which it is a member. Upon receipt of a LAN packet, the device shall ignore packets whose "domain" byte does not match the locally configured value.

3.8.2.1 RULE – Other Uses of the "Domain" Byte Disallowed

Any use of the "domain" byte, other than that specified by Rule 3.8.2, is prohibited.

3.9 RULE – Time Stamps for all Data and Events

All Class A and Class B LXI devices shall assign a timestamp to all measurement data and all direct LAN message events. For direct LAN messages, use of the data packet format in Section 4 satisfies this rule. For measurement data, refer the Section 6. All such timestamps shall be derived from the IEEE 1588 time base. If Class C devices direct LAN message events, they shall supply a time stamp of zero in the LAN packet. If Class C devices implement any part of the standard LXI API that returns a time stamp value, the returned time stamp shall contain a value of zero.
3.9.1 RULE – Time Stamp Accuracy Must Be Provided

All Class A and Class B LXI devices shall include information as to the accuracy of the time stamps that they supply. As a minimum, this information shall be available as part of the documentation that accompanies each LXI device (whether printed or electronic).

3.9.1.1 Permission – Circumstances Under Which Time Stamps May Be Zero

Time stamp values for data may be zero under the following circumstances:

The instrument is a Class C device.

The instrument is overloaded, and cannot capture time stamps fast enough. This condition should be considered a non-fatal error.

Vendors may implement an option to disable the collection of time stamps in a module. In this case, the instrument shall collect time stamps by default, and users must explicitly disable the functionality.

3.9.2 Recommendation – Precision of Time Stamps

Timestamps shall be derived from the IEEE 1588 clock with a precision that is consistent with the event or data acquisition process and the resolution of the clock. For example if the measurement front-end bandwidth is 1 Hz then the timestamp precision should be better than 1 second. If the measurement front-end bandwidth is 1 GHz then the timestamp precision should be better than 1 nanosecond or whatever the local clock supports.

3.9.3 Recommendation – Time Stamp Precision Available via Driver

The precision of the timestamp should be available via a driver call.

3.10 RULE – Internal Log File for Events

All LXI devices capable of acting on or generating LXI LAN events shall be configurable to record a timestamp and event identifier for all transmitted and received module-to-module LAN messages (TCP unicast and UDP multicast) in an internal event log. This event log shall be accessible via a driver transaction from the controller. (See the Programmatic Interface section of the LXI specification for details.)

Logging shall be enabled or disabled via a driver command. The timestamps shall be from the IEEE 1588 clock for Class A and B devices. Class C devices shall either assign a timestamp of 0 or base the timestamp on a time base consistent with the current IEEE 1588 time base of the system.

3.10.1 Recommendation – Events To Be Logged

In addition to the logging required by 3.10, devices should log any events that are significant to the instrument or application domain, including driver commands, triggers of any form, or significant internal state change.

Vendors are allowed and encouraged to log additional events that are appropriate and meaningful for the application domain and device functionality.

Roadmap: A future version of this standard may require the ability to freeze the event log via a software driver API, trigger, LAN event, or other similar mechanism.
3.11 RULE – Follow IEEE 1588 Specification for Clock Management

All remote IEEE 1588 device configuration shall be done via IEEE 1588 management messages.

3.12 FUTURE Rule – Pulse-per-Second Output

A pulse-per-second output shall be available on all LXI devices implementing IEEE 1588. The mechanical and electrical specifications of this output shall be vendor-defined, but the output shall generate a rising edge synchronous with the second’s transitions of the IEEE 1588 clock.

This output, which is derived from the IEEE 1588 clock, is intended to be compared with corresponding outputs of the other clocks in the system to verify synchronization performance. The test point does not need to be available externally, although it can be brought to an external point if desired (for instance, by configuring the LXI trigger bus to carry the signal).

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4 Module-to-Module Data Communications

4.1 Introduction

This section describes the data format for direct module-to-module messages. These messages are data packets that are either multicast on the LAN via UDP or transmitted through a point-to-point TCP connection. Each message is time stamped and signals the occurrence of some event in the system. Instruments in the system can be programmed to broadcast messages (or not) as needed.

This chapter describes a message format that is required for LXI Class A and Class B devices. Module-to-module communication is not a required feature of Class C devices. However, Class C devices may participate in module-to-module communications by using a zero value for the time stamp, as allowed in Permission 3.3.6.

4.2 RULE – Multicast Address and Port Number

Module-to-module communications shall utilize UDP/TCP port number 5044. If utilizing UDP multicasting, data packets shall be transmitted to the UDP multicast address 224.0.23.159.

4.3 RULE – Data Packet Size

For UDP communications, the total size of the data used for module-to-module communications shall not be larger than a single LAN data packet.

4.4 RULE – Data Packet Format

Module-to-module data packets shall contain the following fields. Please see Appendix B for examples.

<table>
<thead>
<tr>
<th>HW Detect</th>
<th>Domain</th>
<th>Event ID</th>
<th>Sequence</th>
<th>Timestamp</th>
<th>Epoch</th>
<th>Flags</th>
<th>Data Fields…</th>
<th>0 (two bytes)</th>
</tr>
</thead>
</table>

Each field is described below. It is assumed that one byte is the standard size, i.e. 8 bits (also referred to as an octet). Furthermore, all multi-byte numeric fields are big-endian (most significant byte comes first). Within each byte (octet) of the fields described above the least significant bit is transmitted first. For octet array fields the most significant field is transmitted first. The most significant array field is the field with index 0.

The above fields shall be marshaled into the on-the-wire format in the following order:

**HW Detect**: Octet array of length 3: Used as a "magic value" to identify valid packets, and also reserved for future hardware detection of module-to-module packets. This field should be set to the value "LXI." Note that the third octet, ASCII "I" is also used as a version identifier; future revisions to this spec may change this value.

**Domain**: One octet, treated as an unsigned byte. The default value is zero.

**Event ID**: Octet array of length 16: Contains an event identifier. This field shall contain the first 16 octets of the event name (an ASCII string) specified in the LXI API. Event names longer than 16 ASCII characters are truncated to the first 16 characters. All event names listed in the table of strings for triggering and synchronization in Rule 6.4.4 that refer to repeated capabilities are...
predefined by the LXI Consortium. All other names are available to users. The leading character shall be in the octet with index 0. For event names of less than 16 characters the unused octets shall be set to 0x00. This field is not NUL-terminated (0x00) but appears so if the event name happens to be less than 16 characters. All 16 octets of this field are significant.

**Sequence:** UInteger32: Contains a sequence number. Each transmitting instrument shall maintain the following independent sequence counter(s):

- One for each combination of UDP multicast network interface and UDP multicast destination port that the instrument supports
- One for each TCP connection.

Upon transmitting a LAN event (module-to-module) message, an instrument shall increment the sequence counter associated with the transport for that message by one.

The initial value of a sequence counter is not defined by this standard and is left up to the vendor.

By specifying how sequence numbers are generated, modules and applications may implement various forms of duplicate packet detection; however, discussion of duplicate packet detection is beyond the scope of this standard.

(Note: If packets are re-transmitted to enhance reliability, re-transmitted packets shall contain the same sequence number as the original.)

**Timestamp:** 10 octets: A time stamp that identifies the time that the event occurred. This time stamp shall use the format specified here:

```c
struct TimeRepresentation
{
    UInteger32 seconds:
    UInteger32 nanoseconds:
    UInteger16 fractional_nanoseconds:
}
```

where

the nanoseconds member is defined such that the most significant bit represents the sign bit, 1 indicating a negative number, and the least significant 31 bits represent the nanoseconds portion of the time being represented. TimeRepresentation thus defines a sign magnitude representation for time stamps and time intervals.

The range of the absolute value of the least significant 31 bits of the nanoseconds portion of the representation shall be restricted to:

\[ 0 \leq |\text{least significant 31 bits of nanoseconds}| < 10^{9} \]

The sign of the nanoseconds member shall be interpreted as the sign of the entire representation.

These fields shall be marshalled into their on-the-wire format in the following order: seconds, nanoseconds, fractional_nanoseconds.

The seconds field shall be marshaled into the on-the-wire format before the nanoseconds field.
For example:
+2.0 seconds is represented by seconds = 0x00000002 and nanoseconds = +0x00000000
-2.0 seconds is represented by seconds = 0x00000002 and nanoseconds = 0x80000000
+2.000000001 seconds        by seconds = 0x00000002 and nanoseconds = 0x00000001
-2.000000001 seconds        by seconds = 0x00000002 and nanoseconds = 0x80000001

The fractional_nanoseconds field is reserved for future expansion of the IEEE 1588 time format. Until the new revision of the IEEE 1588 specification is finalized, this field should be set to zero.

If no event timestamp is available, for example if the event is derived from a legacy device or an LXI Class C device incapable of assigning a timestamp, a time value of 0 (zero) shall be assigned to the timestamp. A value of 0 for a timestamp shall be interpreted as "now," i.e., the time when the recipient handles the message.

**Epoch**: A UInteger16 that contains the IEEE 1588 epoch. Devices incapable of assigning a timestamp (e.g., LXI Class C modules) shall assign a value of zero (0) to the epoch.

**Flags**: UInteger16 that contains data about the packet. Bits within the flag byte are defined as follows:

Bit 0 – Error Message: If set to 1, indicates that this packet is an error message.

Bit 1 – Retransmission: DEPRECATED – THIS BIT SHALL NOT BE USED IN NEW DESIGNS. If set to 1, indicates that this packet is a re-transmission of a prior packet and contains identical information. This allows LXI devices to transmit packets multiple times (for increased reliability), if desired. Modules are not required to implement this feature; however, those modules shall ignore packets if this bit is set.

Bit 2 – Hardware Value: A logical value that characterizes trigger events (particularly hardware events). Refer to the programmatic interface section of the LXI spec for further explanation.

Bit 3 – Acknowledgement: If set to 1, indicates that this packet is an acknowledgement that a prior packet was successfully received. This allows LXI systems to implement UDP-based handshaking protocols (for increased reliability), if desired. Modules are not required to implement this feature; however, those modules shall ignore packets if this bit is set.

Bit 4 - Stateless Event. If set to 0 (required in versions of this standard prior to 1.2), indicates that the contents of Hardware Value (Flags Bit 2) must be monitored by receiving modules. If set to 1, indicates that the event being transmitted is stateless and thus the contents of Hardware Value (Flags Bit 2) must be ignored by receivers.

Bit 5-15 – Reserved. All bits shall be set to zero..

**Data Fields**: Arbitrary number of bytes, up to the capacity of the data packet. Each data field shall be formatted as follows:

*Data Length* (UInteger16): Length of the data that follows. This field shall contain a zero if no further data is contained in the packet.
Identifier (Integer8): A user-definable identifier that specifies the type of data to follow. Numbers from zero to 127 are available for user-defined identifiers.

The LXI Consortium has defined the following Identifier values:

<table>
<thead>
<tr>
<th>Value</th>
<th>Data Type</th>
<th>Length (Octets)</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>-1 (0xFF)</td>
<td>ASCII Data</td>
<td>1</td>
<td>ASCII Character String; not null-terminated</td>
</tr>
<tr>
<td>-2 (0xFE)</td>
<td>int8</td>
<td>1</td>
<td>Two’s-complement</td>
</tr>
<tr>
<td>-3 (0xFD)</td>
<td>uint8</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>-4 (0xFC)</td>
<td>int16</td>
<td>2</td>
<td>Two’s-complement; multi-octet fields are big-endian</td>
</tr>
<tr>
<td>-5 (0xFB)</td>
<td>uint16</td>
<td>2</td>
<td>Multi-octet fields are big-endian</td>
</tr>
<tr>
<td>-6 (0xFA)</td>
<td>int32</td>
<td>4</td>
<td>Two’s-complement; multi-octet fields are big-endian</td>
</tr>
<tr>
<td>-7 (0xF9)</td>
<td>uint32</td>
<td>4</td>
<td>Multi-octet fields are big-endian</td>
</tr>
<tr>
<td>-8 (0xF8)</td>
<td>int64</td>
<td>8</td>
<td>Two’s-complement; multi-octet fields are big-endian</td>
</tr>
<tr>
<td>-9 (0xF7)</td>
<td>uint64</td>
<td>8</td>
<td>Multi-octet fields are big-endian</td>
</tr>
<tr>
<td>-10 (0xF6)</td>
<td>float32</td>
<td>4</td>
<td>IEEE 754 Format; multi-octet fields are big-endian</td>
</tr>
<tr>
<td>-11 (0xF5)</td>
<td>float64</td>
<td>8</td>
<td>IEEE 754 Format; multi-octet fields are big-endian</td>
</tr>
<tr>
<td>-12 (0xF4)</td>
<td>float128</td>
<td>16</td>
<td>IEEE 754 Format; multi-octet fields are big-endian</td>
</tr>
<tr>
<td>-13 (0xF3)</td>
<td>UTF-8 Data</td>
<td>1</td>
<td>Unicode String Data encoded in UTF-8; not null-terminated</td>
</tr>
</tbody>
</table>

For any of the LXI Consortium-defined Identifier values, the Data Length field may be an integer multiple of the data type’s length, indicating that a sequence of values of the indicated data type is stored in the User Data field. Note that the Data Length field is always a length in octets, regardless of the Identifier value.

User Data (succeeding bytes): Data as an octet-array whose length is given by the Data Length field. The value does not include the 1-octet Identifier field itself.

There may be multiple data fields in a module-to-module packet. The packet ends when a zero (two bytes) is encountered as the length of the next field or when the maximum data payload limit is reached.

This variable-length data field is designed to satisfy two different needs. First, it allows the LXI Consortium to define new data fields that may become a part of the LXI specification. Second, it allows vendors to define proprietary data fields of their own. Such proprietary data fields would naturally not be understood by modules from other vendors and should be ignored.

4.4.1 RULE – Use of HW Detect Field

LXI devices shall ignore any received packet that does not contain the ASCII value "LX" in the first two bytes of the HW Detect field of a packet.
4.4.2 RULE – Use of Domain Byte

LXI devices shall maintain an internal configuration option that allows users to specify the value of the Domain field. Upon transmitting a data packet, modules shall copy that value to the Domain field. Upon receiving a data packet, modules shall ignore all packets whose Domain field does not match the module’s own.

4.4.3 RULE – NULL Events

If the EventID field of a data packet contains only zeros, the event shall be considered a “null event.” All LXI instruments shall ignore null events, except that they shall be recorded in log files for debugging purposes.

In parallel with DEPRECATED RULE 3.3.1, the following two rules have been DEPRECATED. The requirements of DEPRECATED RULE 3.3.1 to ignore duplicate packets are impossible to implement in the general case. Explicitly transmitting packets multiple times, with or without the Retransmission Flag set, only compounds this problem. Devices are discouraged from using the Retransmission Flag in sending packets and devices should ignore the Retransmission Flag in received packets. This bit shall not be used in new designs.

4.4.4 DEPRECATED RULE – Re-transmitted Data Packets

One possible way to improve the reliability of UDP data transmissions is to transmit packets more than once. If this is implemented, re-transmitted packets shall contain the same sequence number as the original, but the Retransmission flag (bit 1 of the Flags byte) shall be set.

DEPRECATED RULE 4.4.4.1 restates DEPRECATED RULE 3.3.1.

4.4.4.1 DEPRECATED RULE – Handling Re-transmitted Data Packets

If a module receives a data packet with the Retransmission flag set, the module shall treat the packet the same as any other. If the original packet was previously received, the re-transmitted packet shall be ignored.

4.4.5 RULE – Acknowledgements

One possible way to improve the reliability of UDP data transmissions is to program the receiving module to return an acknowledgement upon receipt of a data packet. If this is implemented, the acknowledgment packet shall have set the Acknowledgement flag (bit 3 of the Flags byte) set. Modules that do not implement this feature shall ignore received data packets if this flag is set.

4.4.5.1 RULE – Handling Acknowledgement Packets

If a module receives a data packet with the Acknowledgement flag set, and the module does not implement a handshaking protocol, then the module shall ignore the packet.

4.5 RULE – Pre-defined Error Messages

Some data packets may contain error messages rather than event notifications. These messages are broadcast on the same address and port as normal event messages, but they set the Error bit (bit
zero) of the Flags field of the data packet. This allows the creation of an “event monitor” tool that can be used for debugging and can quickly identify errors as they occur.

Errors can be further identified by the use of the data fields in the message. This allows specific errors to be identified by an ID number, a descriptive string, or both.

Certain error messages are predefined. For these messages, the Event ID field in the data packet shall be set to “LXIError,” and the specific error identifier is contained in the following data field. The error identifier will be less than zero so that it falls into the LXI-reserved range. Pre-defined error messages are listed in the table below:

<table>
<thead>
<tr>
<th>Identifier</th>
<th>Error definition</th>
<th>Contents of User Data field</th>
</tr>
</thead>
<tbody>
<tr>
<td>-1</td>
<td>Time reset – broadcast whenever the master IEEE 1588 clock has drifted away from UTC (with respect to a central UTC time server)</td>
<td>Time offset that is needed to match UTC time</td>
</tr>
</tbody>
</table>
5 LXI Hardware Triggering

5.1 Introduction

Hardware triggers can be used to initiate predefined actions in an LXI Device, such as generating a signal, making a measurement, or closing a switch. Hardware triggering provides an alternative to, or complements, LAN-based triggering in applications requiring higher precision or lower latency (see Section 6 for more information on LAN-based triggering). LXI Devices send or receive hardware trigger signals using either the LXI Trigger Bus or vendor-specific hardware interconnect lines. The LXI Trigger Bus consists of eight shielded twisted-pair wires that can be used to distribute M-LVDS signals between groups of devices connected in either a daisy-chain, star, or hybrid-star configuration (see Figure 5.1).

![LXI Trigger Bus Configurations](image)

Figure 5.1 LXI Trigger Bus Configurations
Several examples of possible hardware trigger scenarios are listed below:

- An external event generates a trigger signal that initiates an action in a device.
- A computer generates a trigger signal that initiates an action in a device.
- One device sends a trigger signal to one or more other devices that initiates simultaneous actions in those devices. (From the triggered device’s perspective, this is the same as an external event.)
- A hardware trigger signal received by one device (either from another device or an external event) causes it to send any number of LAN-based triggers to one or more other devices, initiating preconfigured actions in those devices.
- LAN-based triggers received by one or more devices cause hardware triggers to be sent to one or more other devices, initiating actions in those devices.
- An external event generates a hardware trigger signal that is time-stamped by a device and used to capture data from a circular buffer prior to the trigger.
- A common reference clock, distributed using hardware trigger lines, is used to synchronize actions in multiple devices.

Each channel of the LXI Trigger Bus is capable of operating in one of two modes, the mode being set by programming the LXI Devices that are taking part in the trigger operation for that channel. LXI Devices that are not taking part in a trigger operation should have their drivers disabled.

The two modes of operation are:

- **Driven Mode.** This provides point-to-multipoint operation. One device initiates a trigger event to one or more receiving devices. This mode uses one driver per LXI Device for each Trigger Bus channel.

- **Wired-OR Mode.** This is a multipoint-to-multipoint operation. One or more devices initiate a trigger event to one or more receiving devices. In this mode, the event can be initiated by the first device to trigger (first device to recognize an event starts others to perform tasks), or the last device to trigger (last device ready initiates others to perform tasks). The Wired-OR Mode requires one device to be configured as the Wired-OR Bias Device to provide a bias for the wired trigger channel. Other driver devices participating in the wired trigger require the use of two drivers for each Trigger Bus channel.

### 5.2 Functional Class Requirements

#### 5.2.1 RULE – LXI Trigger Bus Required for Class A

LXI Devices conforming to Functional Class A shall provide appropriate hardware triggering by generating and/or responding to trigger events utilizing the LXI Trigger Bus interface.

#### 5.2.1.1 Permission – LXI Trigger Bus Optional for Class C

LXI Devices conforming to Functional Class C may optionally incorporate LXI Trigger Bus functionality, providing all relevant requirements set forth in Section 5 are satisfied.
5.2.1.2 Permission – Vendor-Specific Hardware Trigger Interfaces

Optional, vendor-specific hardware trigger interfaces (e.g., BNC, SMB, etc.) are permitted on all functional classes of LXI Devices. Optional trigger ports may provide selectable inputs or outputs on either the front or rear of the device.

5.3 Electrical Requirements

5.3.1 RULE – Number of Channels

The LXI Trigger Bus shall consist of eight individual hardware channels.

5.3.2 RULE – Signaling Standard

Each LXI Trigger Bus channel shall use half-duplex, Multipoint-Low-Voltage-Differential Signaling (M-LVDS) with Type-1 receivers, compliant with TIA/EIA-899.

5.3.3 RULE – Maximum Number of Nodes per Segment

The maximum number of nodes on any LXI Trigger Bus connection segment shall be 16.

5.3.4 RULE – LXI Trigger Bus Buffering

Each LXI Device connected to the LXI Trigger Bus shall provide half-duplex buffering on each channel, between the external M-LVDS pair and the internal signal routing of the LXI Device.

5.3.5 RULE – M-LVDS Transceiver Type

One of the following M-LVDS transceivers shall be used for the LXI Trigger Bus: Texas Instruments SN65MLVD080 (8 channel) or Texas Instruments SN65MLVD201 (single channel).

5.3.5.1 Permission – M-LVDS Transceiver Type

Vendors may use an equivalent device but must provide a technical justification for its use that demonstrates it has equivalent performance for LXI Trigger Bus application.

5.3.6 RULE – Input/Output Configurability

Each LXI Trigger Bus channel shall be individually configurable as an input or output (or both), and shall be capable of being individually enabled or disabled.

5.3.7 RULE – Driver Mode Configurability

Each LXI Trigger Bus driver shall be individually configurable to operate in either Driven or Wired-OR Mode.
5.3.8 RULE – Driver Topology

Each LXI Trigger Bus driver shall consist of two M-LVDS drivers with the outputs connected in parallel, as shown in Figure 5.2 b). In Driven Mode, only one driver shall be enabled, and the trigger signal shall be applied to the driver’s input. In Wired-OR Mode, each driver shall be configured to drive current from the positive (A) output to the negative (B) output when enabled, and the trigger signal shall be applied to the enable inputs of both drivers.

Figure 5.2 a) Single LXI Trigger Bus Channel with Bus Terminators (*See Fig 5.2 b)

Figure 5.2 b) LXI Trigger Bus Drivers, Driven and Wired-OR Modes

5.3.9 RULE – Wired-OR Bias

Each LXI Trigger Bus channel configured for Wired-OR Mode operation shall be provided a Wired-OR Bias by any one of the LXI Devices connected to the bus.

5.3.10 RULE – Wired-OR Bias Device

Each LXI Device connected to the LXI Trigger Bus shall be capable of being configured to act as the Wired-OR Bias Device for any number of LXI Trigger Bus channels configured for Wired-OR operation, by providing the Wired-OR Bias for those channels. The Wired-OR Bias Device shall be capable of enabling and disabling the Wired-OR Bias under programmatic control, on a channel-by-channel basis.

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5.3.11 RULE – Wired-OR Bias Device Functionality

The LXI Trigger Bus driver of an LXI Device that is configured to act as the Wired-OR Bias Device for a particular channel shall always operate in Driven Mode, and shall be continuously enabled to drive the Trigger Bus channel low (negative), unless it is participating in the wired-OR communication, in which case it shall actively drive the bus according to its input. (Refer to the Driven-Mode Driver schematic in Figure 5.2b).

5.3.12 RULE – Power-up Default Configuration

All LXI Trigger Bus channels shall default to the disabled configuration when power is applied to the device.

5.3.13 RULE – Configurable Edge or Level Detection of Signals

LXI Trigger Bus signals shall be programmable for positive or negative edge, or positive or negative level detection, on a channel-by-channel basis.

5.3.14 RULE - Signal Routing to All Eight Channels

Any LXI Device capable of transmitting or receiving signals on the LXI Trigger Bus shall be capable of doing so on any of the eight Trigger Bus channels.

5.3.15 RULE – Simultaneous Transmit and Receive

LXI devices using the LXI Trigger Bus shall be capable of simultaneously transmitting and receiving signals on any of the eight Trigger Bus channels.

5.3.16 Recommendation – Gating of Unwanted Receiver Outputs

In cases when it is not possible to disable individual receivers, unwanted receiver output signals should be gated-off inside the LXI device, using additional logic.

5.3.17 RULE– Minimum Pulse Width in Driven Mode

The minimum pulse width of LXI Trigger Bus signals transmitted in Driven Mode on connection segments of 10 meters or less shall be 10 ns, and shall be 20 ns on connection segments of 20 meters or less.

5.3.18 RULE – Minimum Pulse Width in Wired-OR Mode

The minimum pulse width of LXI Trigger Bus signals transmitted in Wired-OR Mode on connection segments of 10 meters or less shall be 20 ns, and shall be 40 ns on connection segments of 20 meters or less.

5.3.19 RULE – Documentation of Minimum Trigger Pulse Width

Manufacturers shall provide documentation specifying the minimum pulse width required by an LXI Device to achieve reliable triggering when using edge detection.
5.3.20 Recommendation – Ready Signal

LXI Devices should provide an output signal indicating when the device is “ready.” For a measuring device, this means that it is in the “waiting for trigger” or “armed” condition. For a source type device, it indicates the output signal is stable and within specification. This signal, if available, should be accessible over the LXI Trigger Bus or via LAN.

5.3.21 Recommendation – Measurement Complete Signal

LXI Devices should provide an output signal indicating when the device has completed a measurement. This signal, if available, should be accessible over the LXI Trigger Bus or via LAN.

5.4 Physical Requirements

5.4.1 RULE – LXI Trigger Bus Connector Type

25-pin Micro-D connectors shall be used to interconnect LXI Devices incorporating the LXI Trigger Bus.

5.4.2 Recommendation – LXI Trigger Bus Connector Type

The following connectors, or equivalents, are representative of the type recommended for LXI Trigger Bus connectivity: ITT Cannon MDSM-25PE-Z10-VR17 (single connector) or Molex 83619-9011 (dual connector). These connectors can be double-stacked in a 1U configuration for efficient space utilization.

5.4.3 RULE – Number of LXI Trigger Bus Ports

All LXI Devices implementing the LXI Trigger Bus (except devices conforming to all of the additional requirements of a Star Hub) shall have at least one LXI Trigger Bus port, consisting of a pair of LXI Trigger Bus connectors wired in parallel (like-numbered pins connected together). (See Section 2 for recommended connector locations).

5.4.4 Permission – Additional LXI Trigger Bus Ports

LXI Devices may have more than one LXI Trigger Bus port, provided that each port consists of a single pair of LXI Trigger Bus connectors wired in parallel (like-numbered pins connected together), and that each port provides the required buffering, as defined in Section 5.3.

5.4.5 RULE – Trace Characteristic Impedance

Traces interconnecting the LXI Trigger Bus connector pins shall be designed for 100-ohms (±10%) differential characteristic impedance.

5.4.6 RULE – Printed Circuit Trace Lengths

Traces interconnecting the LXI Trigger Bus connector pins shall be kept as short as possible, with a maximum trace length of 63.5 mm (2.5 inches) between connectors.
5.4.7 RULE – Channel-to-Channel Skew

Traces interconnecting the LXI Trigger Bus connectors shall be kept as equal in length as possible, with a trace length difference between channels (maximum-to-minimum) of less than 25 mm.

5.4.8 RULE – Maximum Stub Length

If a stub is created as a result of connecting the LXI Trigger Bus to a transceiver, the maximum stub length shall not exceed 12.7 mm (0.5 inches).

5.4.9 RULE – LXI Trigger Bus Connector Pin Assignments

LXI Trigger Bus connectors and cables shall use the pin assignments shown in Table 5.1.

5.4.9.1 RULE – +3.3V Supply on LXI Trigger Bus Connectors

Each LXI Trigger Bus connector shall provide +3.3 V (± 0.2 V), capable of sourcing a total minimum current of 100 mA for both connectors (not 100 mA on each connector). The power supply shall be short circuit protected.

5.4.10 Recommendation – +3.3V Protection Using Self-Healing Fuse

The +3.3V power pin on each LXI Trigger Bus connector should be protected by a separate self-healing fuse.

5.4.11 RULE – Reserved Pins Not To Be Used For Other Purposes

Pins designated as “Reserved” shall not be used for any purpose that is not specifically authorized by the LXI Consortium.

Table 5.1 LXI Trigger Bus Pin Assignments

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>+3.3V</td>
</tr>
<tr>
<td>2</td>
<td>+3.3V RETURN</td>
</tr>
<tr>
<td>3</td>
<td>LXI1p</td>
</tr>
<tr>
<td>4</td>
<td>LXI1n</td>
</tr>
<tr>
<td>5</td>
<td>GND</td>
</tr>
<tr>
<td>6</td>
<td>LXI3p</td>
</tr>
<tr>
<td>7</td>
<td>LXI3n</td>
</tr>
<tr>
<td>8</td>
<td>GND</td>
</tr>
<tr>
<td>9</td>
<td>LXI5p</td>
</tr>
<tr>
<td>10</td>
<td>LXI5n</td>
</tr>
<tr>
<td>11</td>
<td>Reserved</td>
</tr>
<tr>
<td>12</td>
<td>LXI7p</td>
</tr>
<tr>
<td>13</td>
<td>LXI7n</td>
</tr>
<tr>
<td>14</td>
<td>LXI0p</td>
</tr>
<tr>
<td>15</td>
<td>LXI0n</td>
</tr>
<tr>
<td>16</td>
<td>Reserved</td>
</tr>
<tr>
<td></td>
<td></td>
</tr>
<tr>
<td>---</td>
<td>---</td>
</tr>
<tr>
<td>17</td>
<td>LXI2p</td>
</tr>
<tr>
<td>18</td>
<td>LXI2n</td>
</tr>
<tr>
<td>19</td>
<td>GND</td>
</tr>
<tr>
<td>20</td>
<td>LXI4p</td>
</tr>
<tr>
<td>21</td>
<td>LXI4n</td>
</tr>
<tr>
<td>22</td>
<td>GND</td>
</tr>
<tr>
<td>23</td>
<td>LXI6p</td>
</tr>
<tr>
<td>24</td>
<td>LXI6n</td>
</tr>
<tr>
<td>25</td>
<td>Reserved</td>
</tr>
</tbody>
</table>

Connector Shell | CHASSIS

Note: LXI Trigger Bus signals with a “p” suffix are the positive (A) half of the pair and those with an “n” suffix are the negative (B) half.

5.5 Specific Requirements for Star Hubs

5.5.1 RULE – Star Hub Number of Ports

A Star Hub shall have a minimum of three LXI Trigger Bus ports.

5.5.2 RULE – Star Hub Signal Buffering

Each channel of each Star Hub port shall provide half-duplex buffering between the external M-LVDS pair and the hub’s internal signal routing.

5.5.3 RULE – Star Hub Internal Bus Termination, Single-Connector Ports

Star Hub ports implemented with a single connector shall provide internal, integrated termination resistors on all eight LXI Trigger Bus channels, identical to those defined in Rule 5.6.1.

5.5.4 RULE – Star Hub Minimum Signal Routing Capability

A Star Hub shall be capable, at a minimum, of receiving a Trigger Bus signal on any channel of any port and simultaneously retransmitting that signal on the same channel of any number of other ports.

5.6 LXI Trigger Bus Cables and Terminators

5.6.1 RULE – LXI Trigger Bus Termination

The LXI Trigger Bus shall be terminated using termination connector blocks installed at both ends of a connection segment, as shown in Figure 5.1. Each individual channel shall be terminated at each end by two 50-ohm (±5 %) resistors connected in series between the positive (A) and negative (B) signal wires, and a 0.01µF capacitor to ground connected to the node between the resistors, as shown in Figure 5.2 a). The resistor values shall be matched to within ±2%.

5.6.2 RULE – LXI Trigger Bus Cable and Terminator Specifications

LXI Trigger Bus cables and terminators shall be constructed in conformance with the specification contained in the LXI Consortium document entitled “LXI Trigger Bus Cable and Terminator Specifications”.

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6 LXI Programmatic Interface (Drivers)

The following rules will guide the software characteristics of LXI devices. Software synergy is important to ensure LXI devices are easy to integrate with each other and the test program.

Customers need a single standard driver to ensure interoperability.

6.1 RULE – IVI Driver Requirement

All LXI devices shall provide an IVI Specific Driver. The details of this requirement are called out in Section 5 of IVI-3.11. If an LXI device is a reasonable match to an existing IVI Class specification, its driver shall be class compliant.

6.1.1 RULE – Trigger and Event Required API

IVI drivers for LXI devices shall conform to the IVI-3.15 IviLxiSync specification to satisfy the API requirements in this section of the LXI specification. Therefore any LXI device that implements the LXI trigger bus or LXI events (and triggers) must implement the corresponding APIs.

6.1.2 Recommendation – IVI-COM Recommendation

Although the LXI Foundation has chosen not to require a particular driver technology, there is a significant customer benefit in LXI devices providing a consistent driver solution. Therefore, LXI device vendors are encouraged to provide IVI-COM drivers with their devices. IVI-COM drivers provide excellent tools for customers that want to achieve interchangeability and are based on the Microsoft COM technology which is supported by all major application development environments.

LXI vendors wishing to optimize the customers experience in National Instruments LabVIEW or National Instruments LabWindows should consider also providing G drivers\(^1\) or IVI-C drivers respectively.

6.1.2.1 Permission – Provide Other Drivers as Needed

LXI devices may optionally provide additional drivers. This is especially appropriate for operating environment other than Microsoft Windows (e.g., LINUX, VxWorks, UNIX, etc.). The LXI Consortium will not do explicit specification work to support these alternate drivers.

6.2 RULE – Syntax of the Device Address

LXI IVI Drivers shall accept VISA resource names.

The IVI driver provided with an LXI device may use whatever underlying protocol is permitted by Section 8, LXI LAN Requirements. However, the driver shall accept any valid VISA resource name as the network resource location as described in this section.

---

\(^1\) For more information on IVI or to download the specifications, see www.ivifoundation.org

\(^2\) Also known as VXIplug&play GWIN Framework drivers

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Specifically, valid VISA resource names for LAN instruments are:

TCPIP[board]::host address[::LAN device name][::INSTR]
TCPIP[board]::host address::port::SOCKET

Where:
- **board** is an integer representing a physical network interface card in the computer
- **host address** is either a hostname or IP address (4 bytes in decimal separated by “.”)
- “INSTR” is the resource class. It implies a protocol that supports read, write, trigger, status, and clear
- “SOCKET” is the resource class. It implies a protocol based on a raw tcp/ip connection that may only support read/write.

Although VISA does not specify that the data being read/written to the device is an ASCII instrument control language (such as SCPI), it is implied by the INSTR and SOCKET resource classes.

If the driver supports control of the device via either the SOCKET or INSTR protocols, the driver shall use the specified protocol, unless a subsequent driver call or initialization string alters that behavior.

The driver shall choose the most appropriate protocol for controlling that device. For the INSTR resource class the LAN device name may be used to specify a port. If the IP port, the LAN device name, or resource class is not relevant for that protocol, the driver shall ignore the irrelevant parameters.

Note that this resource descriptor may be passed directly by the customer to the open call or it may be extracted from the IVI Configuration Store.

### 6.3 RULE – IVI Property for Referencing a Signal Source

Any IVI interface referencing an LXI Trigger Bus signal or LXI LAN event as an input shall have a property of type BSTR named Source, or ending in Source, if there is a prefix. All actions within a device which can be triggered by an LXI Trigger Bus line, 1588 alarm, or LAN event shall be configurable via an interface that has a source property as stated above.

---

3 For additional information see vpp43.doc at http://www.ivifoundation.org/Downloads/Specifications.htm
6.4 RULE – Eight LAN Events for Arm/Trigger and Eight for Events

Class A and class B devices having an Arm-Trigger state machine shall provide a minimum of eight LAN event inputs for arm and trigger purposes and eight LAN event outputs for signaling other devices.

6.4.1 Recommendation – Adding Additional Arm/Trigger Sources and Events

Class A and class B devices should provide extensibility in their Arm, Trigger, and Event interfaces using Add() and Remove() methods.

6.4.2 RULE –IVI-3.15 IviLxiSync API Routes Events to LAN

All LXI devices capable of routing a signal to the LXI Trigger Bus or to the LAN Event Sender shall do so using the IVI-3.15 IviLxiSync API.

6.4.3 RULE – LAN Events Encode the Sense of the Event in Packet

All devices transmitting LAN Events whose signal source (the signal causing the event) is:

- one of the LXI Trigger Bus lines,
- one of the signals from an Arm-trigger state machine, or
- based on a logical signal within the device

shall encode the state of the source signal immediately following the transition that caused the event in Flag Bit 2 (Hardware Value)—which is reserved for the logical value of the event signal—and set Flag Bit 4 (Stateless Event) to 0. Hence, a rising edge transition records a logical one and a falling transition records a logical zero.

All devices transmitting LAN events whose signal source is not based on logical signals as described in the previous paragraph (i.e., they are stateless or have some other semantics) shall have Flag Bit 4 (Stateless Event) set to 1.
6.4.4 RULE – Standard Strings Used to Designate Events

The strings listed in the following tables shall be used as indicated for triggering, synchronization, and event generation purposes. Devices are not required to implement all signals. Signal names are case sensitive.

Note: This state machine example is not a part of Rule 6.4.5.

Example Arm-Trigger State Machine (for signal name reference purposes).
Arm-Trigger State Machine Signal Relationships:

**OperationComplete**

**Sweeping**

**WaitingForArm**

**WaitingForTrigger**

**Measuring**

**Settling**

(May Occur multiple times)

**Note:** These strings are for Triggering and Synchronization. They are case sensitive.

<table>
<thead>
<tr>
<th>String</th>
<th>Usage</th>
</tr>
</thead>
<tbody>
<tr>
<td>LXI0</td>
<td>All repeated capability names referring to LXI trigger bus line 0. All Source properties needing to refer to LXI trigger bus line 0. As a signal Source in the IIviEvents interface.</td>
</tr>
<tr>
<td>LAN0</td>
<td>All repeated capability names referring to LAN event 0. All Source properties needing to refer to LAN event 0. This is the LAN analog to LXI0. As a signal Source in the IIviEvents interface. LAN Event Message shall have Stateless Event (Flags Bit 4) set to 0 (zero).</td>
</tr>
<tr>
<td>LXI1</td>
<td>All repeated capability names referring to LXI trigger bus line 1. All Source properties needing to refer to LXI trigger bus line 1. As a signal Source in the IIviEvents interface.</td>
</tr>
<tr>
<td>LAN1</td>
<td>All repeated capability names referring to LAN event 1. All Source properties needing to refer to LAN event 1. This is the LAN analog to LXI1. As a signal Source in the IIviEvents interface. LAN Event Message shall have Stateless Event (Flags Bit 4) set to 0 (zero).</td>
</tr>
<tr>
<td>LXI2</td>
<td>All repeated capability names referring to LXI trigger bus line 2. All Source properties needing to refer to LXI trigger bus line 2. As a signal Source in the IIviEvents interface.</td>
</tr>
<tr>
<td>LAN2</td>
<td>All repeated capability names referring to LAN event 2. All Source properties needing to refer to LAN event 2. This is the LAN analog to LXI2. As a signal Source in the IIviEvents interface. LAN Event Message shall have Stateless Event (Flags Bit 4) set to 0 (zero).</td>
</tr>
<tr>
<td>LXI3</td>
<td>All repeated capability names referring to LXI trigger bus line 3. All Source properties needing to refer to LXI trigger bus line 3. As a signal Source in the IIviEvents interface.</td>
</tr>
<tr>
<td></td>
<td>All repeated capability names referring to LAN event 3. All Source properties needing to refer to LAN event 3. This is the LAN analog to LXI3. As a signal Source in the IIviEvents interface. LAN Event Message shall have Stateless Event (Flags Bit 4) set to 0 (zero).</td>
</tr>
<tr>
<td>---</td>
<td>---</td>
</tr>
<tr>
<td>LXI4</td>
<td>All repeated capability names referring to LXI trigger bus line 4. All Source properties needing to refer to LXI trigger bus line 4. As a signal Source in the IIviEvents interface.</td>
</tr>
<tr>
<td>LXI5</td>
<td>All repeated capability names referring to LXI trigger bus line 5. All Source properties needing to refer to LXI trigger bus line 5. As a signal Source in the IIviEvents interface.</td>
</tr>
<tr>
<td>LXI6</td>
<td>All repeated capability names referring to LXI trigger bus line 6. All Source properties needing to refer to LXI trigger bus line 6. As a signal Source in the IIviEvents interface.</td>
</tr>
<tr>
<td>LXI7</td>
<td>All repeated capability names referring to LXI trigger bus line 7. All Source properties needing to refer to LXI trigger bus line 7. As a signal Source in the IIviEvents interface.</td>
</tr>
<tr>
<td>LXIERROR</td>
<td>All repeated capability names referring to LXI defined error events. As a signal Source in the IIviEvents interface. LAN Event Message shall have Stateless Event (Flags Bit 4) set to 1.</td>
</tr>
</tbody>
</table>

**Note:** These strings are for Event Generation. They are case sensitive

<table>
<thead>
<tr>
<th>String</th>
<th>Usage</th>
</tr>
</thead>
<tbody>
<tr>
<td>OperationComplete</td>
<td>Used as a signal Source in the IIviEvents interface. In the Arm-Trigger state machine: this signal is set false when transitioning from the Idle state to the Initiated state. It is set true when transitioning from the initiated state into the Idle state. LAN Event Message shall have Stateless Event (Flags Bit 4) set to 0 (zero).</td>
</tr>
<tr>
<td>Measuring</td>
<td>Used as a signal Source in the IIviEvents interface. In the Arm-Trigger state machine of a measuring device: this signal is set true when transitioning out the bottom of the Trigger state. It is set false when transitioning into the Trigger state from below. LAN Event Message shall have Stateless Event (Flags Bit 4) set to 0 (zero).</td>
</tr>
</tbody>
</table>
### Settling

Used as a signal Source in the IIviEvents interface.
In the Arm-Trigger state machine of a source or signal conditioning device: this signal is set true when transitioning out the bottom of the Trigger state. It is set false when transitioning into the Trigger state from below.
LAN Event Message shall have Stateless Event (Flags Bit 4) set to 0 (zero).

### Sweeping

Used as a signal Source in the IIviEvents interface.
In the Arm-Trigger state machine: this signal is set true when transitioning from the Initiated state to the Arm state. It is set false when transitioning from the Arm state into the Initiated state.
LAN Event Message shall have Stateless Event (Flags Bit 4) set to 0 (zero).

### WaitingForArm

Used as a signal Source in the IIviEvents interface.
In the Arm-Trigger state machine: this signal is set true in the Arm state to enable the Arm logic. It is set false when transitioning from the Trigger state into the Arm state.
LAN Event Message shall have Stateless Event (Flags Bit 4) set to 0 (zero).

### WaitingForTrigger

Used as a signal Source in the IIviEvents interface.
In the Arm-Trigger state machine: this signal is set true in the Trigger state to enable the Trigger logic. It is set false after a trigger has been received.
LAN Event Message shall have Stateless Event (Flags Bit 4) set to 0 (zero).

### All

Used as a hostname in the Event destination. This implies the use of a UDP multicast packet to send the event.

---

### 6.4.4.1 RULE – Only Signals Corresponding to Implemented Capability Required

Devices which only implement a portion of the Arm-trigger state machine shall only be required to implement those signals relating to the implemented portion.

### 6.4.4.2 RULE – Devices Shall Document Supported Signals

Every device shall document which signals are supported.

### 6.4.5 RULE – Event Names Beginning with LXI Reserved

The LXI Consortium reserves all strings used for LAN event names beginning with LXI for future standardization. Such strings shall not be used for any LAN event or trigger name that is not sanctioned by the consortium.

### 6.4.6 RULE – Destination Path Syntax

Destination path syntax for LXI events shall be ([ ] denote optional items):

\[
<\text{Destination Path}> ::= [host[:port]][:name],[<\text{Destination Path}>]
\]

Defaults for the optional items are:

**host**
- The local device (most appropriate for LXI trigger bus events).
- Host 'All' sends a UDP Multicast packet to all devices using the IANA registered host address for LXI events.
- Any other explicit host entry sends events via a TCP stream connection.

**port**
- The IANA registered port (5044) for LXI events.

**name**
- The Item string parameter used to select this event. This is the name associated with the event object.
6.4.7 Recommendation – Create TCP Event Connections in Advance

LXI LAN events sent via TCP streams should build the TCP connection when the event enable is set true and should tear down the connection when the enable is set false. This minimizes the latency to transmit the event to the receiver at time of occurrence.

6.5 RULE – API Shall Represent Time as Two 64-bit Floats

All IVI interfaces shall represent 1588 time, time-stamps, or alarms as two 64-bit floating point numbers. One containing the seconds portion and one containing the fractional seconds.

6.5.1 RULE – Property Names for Real-Time Representation

All interfaces for setting or retrieving 1588 time or alarms derived from 1588 time shall have two properties of type DOUBLE named TimeSeconds and TimeFraction.

6.5.2 RULE – Property Names for Real-Time Time Stamp

Class A and class B devices shall provide two properties of type DOUBLE named TimeStampSeconds and TimeStampFraction in all interfaces that are capable of querying measured data from the device for the purpose of retrieving the time stamp associated with said data. These properties shall be read only.

6.5.2.1 Recommendation – Use a Single Time Stamp for Data Sets

If the interface for returning measurement data provides a summary data set in which it is appropriate to include the time stamp, device designers are encouraged to use this means for associating the time stamp with the data, rather than adding two properties to the interface as it couples the data with the time stamp more securely.

6.6 RULE – Domain Property to Facilitate Multiple Systems on a Single LAN

All LXI devices implementing LAN events shall include a property named LXIDomain of type LONG for the purpose of setting the LXI domain flag transmitted and received in all LXI LAN events. The allowed range of this property is 0 – 255. The factory default value for this property shall be zero.

6.6.1 Recommendation – Domain Property Is Persistent

The value of the LXIDomain property should persist through power cycles of the device.

Recommendation – Location of Domain Property in API

The LXIDomain property should be placed in the same interface that contains the instrument I/O object (if present). This is commonly named System (often with a prefix).

6.7 RULE – Devices Implement UDP and TCP Listeners for Events

Class A and class B devices shall implement a UDP port listener (multicast capable) and a TCP socket listener for the purposes of receiving LXI LAN event packets. The TCP listener shall be
capable of at least 8 simultaneous connections. These listeners shall default to the IANA registered port (5044) for LXI LAN events—user configuration may override this default.

6.7.1 RULE – LAN Event Interpolation

When transmitting LAN Event Messages with a Stateless Event (Flags Bit 4) value of 0 (zero), devices shall behave as follows:

- when transmitting LAN Event Messages configured by the API to be in Wired-OR mode the device shall transmit only a single sense of the event in Hardware Value (Flags Bit 2)
- otherwise, devices shall send both senses of the event in Hardware Value (Flags Bit 2)

When the value of Stateless Event (Flags Bit 4) is zero, recipients of events are required to compare the sense of incoming events with the current state of that event. If the received event sense (value of Hardware Value – Bit 2) is identical (true or false) to the current state of the event, recipients must interpolate an opposite sense event occurring immediately prior to the received event and behave accordingly.

Note: The Wired-OR mode of transmission for LAN events does not implement true wired-OR logic in the way that the Wired Trigger Bus does. Wired-OR mode has no mechanism for detecting the absence of all signals (the false state) because it is impractical to keep an infinite buffer of all packets received.

6.7.2 RULE – Devices Sourcing LAN Events Shall Support UDP and TCP

All LXI devices sourcing LAN events shall provide both a UDP Multicast and a TCP Stream transmission mechanism.

6.7.3 RULE – Devices Use Standard LXI Multicast Address

Class A and class B devices shall use the IANA registered multicast address for LXI UDP event messages.

6.8 RULE – Event Log

Devices shall include an Event Log as required by Rule 3.10 that records all events recognized by the device. The log shall be provided by the API as an array of strings. The following capabilities shall be provided by the API:

- A Boolean property to enable or disable logging
- A method without parameters that clears the log
- A method that returns one or more log entries as a single string, with the instrument or driver deciding how many entries to return.

Additionally, the event log shall expose some method to switch between the blocking and non-blocking behaviors described in Rule 6.8.1. This control is not currently specified by the API, but it may be in the future.

6.8.1 RULE – Event Log Semantics

Devices that include the event log (defined in Rule 3.10 – Internal Log File for Events) shall behave as follows:
The event log shall behave as a FIFO buffer, with new entries appended to the end of the buffer and the oldest entries removed from the beginning of the buffer when the buffer is read by a client.

The size of the event log buffer is device dependent.

If the event log overflows, the device shall include an entry in the log indicating that one or more entries were missed.

Devices may optionally require that logging be disabled before reading back the log.

Devices shall support both an overwriting and non-overwriting mode of operation when the event log is full. When a new entry is added into a full log in overwriting mode, the oldest entry in the log is first discarded, thereby making room for the new entry, allowing the new entry to be appended. When a new entry is added into a full log in non-overwriting mode, the new entry is discarded, leaving the log untouched. Vendors shall expose some method (e.g., option strings, web interface, front panel, etc.) to provide the control for this feature, although no particular API is required. A future version of this specification may include such an API requirement.

Once a log entry is read, it shall be removed from the device’s log.

6.8.2 RULE – Format of the Event Log

The event log shall return a string for each entry in the event log.

A future version of this specification may include a format for the event entry.

6.9 Recommendation – Control Identification Light

Devices should include a programmatic interface to control the IDENT indication (part of the LAN Status indicator). This should be implemented as a Boolean property. For details of the behavior of the IDENT indicator, see 8.11.

6.10 Roadmap Requirements for Locking

The rules, permissions, and observations in this section are preliminary and not a part of the present version of the LXI Standard. These will be refined and completed, when sufficient testing and design has been completed to provide appropriate locking behavior.

Specific areas of concern are:

Providing a way to “lock” a device so that device-device events can be appropriately enabled and disabled, while still providing the desired lock semantics for conventional clients.

Determining how locking capabilities can be mapped across the various protocols devices are likely to use. Specifically, there is a need to provide reasonable assurance that any given communication to a device is from the holder of the lock.

Prototyping and doing multi-vendor testing that cannot be completed for the present version, because of the complexity and related security issues.
6.11 Roadmap RULE – All Protocols Can Guarantee Any Client Exclusive Access

The device has to provide locking for whatever protocols it implements. Note that all supported protocols and APIs need to share a single lock in order to provide any client exclusive access.

6.11.1 Roadmap RULE – Every Interface Supports Locking

Every supported interface to the device must support acquiring and releasing the single device lock. All device protocols must take reasonable precautions to verify that the requestor is truly holding the lock before taking actions that require a lock.

6.11.1.1 Roadmap Permission – Non-Impacting APIs Do Not Require the Lock

A device may permit a client that does not have the lock to access semantics that will not impact a client that is holding a lock.

6.11.2 Roadmap RULE – Web Interface Shall Provide a Way to Break the Lock

The web interface shall support a way to break the lock.

6.11.3 Roadmap Recommendation – Any Client Can Access an Unlocked Device

The API or Protocol should permit a client that does not hold a lock full access to the device semantics, if no other clients hold a lock.

6.11.4 Roadmap RULE – Locks Released When the Session is Closed

The action of closing a session or connection shall release all locks acquired by it.

6.11.5 Roadmap RULE – Specific Hosts or Hosts and Ports May be Disabled

All port and socket event listeners in any given device shall provide a means to filter or restrict incoming events to a specific list of hosts or hosts and ports for each destination within the device.

6.11.6 Roadmap Permission – VXI-11 Locking Is Suitable for VXI-11 Controlled Devices

If and LXI device is a VXI-11 SCPI device, it may use VXI-11 locking so long as the locking semantics extend to other APIs as noted above.
7 LAN Specifications

7.1 RULE – Ethernet Required

LXI devices shall implement Ethernet using the appropriate IEEE 802.x PHY/MAC specification. For a physical connection this shall be IEEE 802.3.

7.1.1 Recommendation - Gigabit Ethernet

LXI devices should support Gigabit (1000Base-T) Ethernet. Gigabit Ethernet will negotiate down to 100Base-T.

7.1.2 RULE – Proper Operation in Slower Networks

LXI Modules shall operate properly in Ethernet networks of equal or slower speed than themselves.

7.2 RULE – MAC Address Display

LXI devices shall display the MAC address of the module via a user-accessible display or label affixed to the module. The MAC address is not changeable.

7.2.1 Recommendation – MAC Address Visible While in Rack

The MAC address should be viewable while the module is in a rack.

7.3 RULE – Ethernet Connection Monitoring

Modules shall incorporate Ethernet connection monitoring (one possible implementation of which is commonly known as Media Sense in Microsoft operating systems). Upon detecting a connection event, the current IP configuration shall be validated (including duplicate IP address detection) and, if necessary, updated.

7.3.1 Recommendation – Preserve Network Connections When Possible

Network connections should be preserved through an Ethernet disconnection and reconnection to the same network. Windows Media Sense uses an Ethernet disconnection as a trigger to tear down any existing network connections.

The preferred behavior is more forgiving and does not tear down existing network connections whenever possible. The preferred steps are:

1. Monitor the Ethernet port periodically to make sure it is still connected to a network by looking for link integrity.
   - If link integrity goes away for less than 20 seconds, then assume the user is just changing Ethernet cabling, and return to checking for link integrity.
   - If link integrity goes away for more than 20 seconds, then assume the user has disconnected from the network and continue to step 2.
2. Monitor the Ethernet port periodically waiting to be reconnected to another (or the original) network. Once it is reconnected (link integrity appears again), continue to the next step.
3. If automatic IP address acquisition is enabled, try to renew the DHCP lease or get a new Dynamic Link-Local Addressing address.
4. If the same IP address is returned, then continue using that IP address and leave all existing connections to the module unchanged.
5. If a different IP address is returned, then start using the new IP address (and all other new configuration values), and clean up any connections involving the old IP address.

If a manual IP address is in force, reuse it along with the subnet mask, default gateway, and DNS server IP addresses that have been manually entered. The software will validate that the manual IP address is still not a duplicate IP address on the network.

7.3.1.1 RULE – User Control over Connection Monitoring Behavior

If a module implements Recommendation 7.3.1, the user shall be able to enable or disable this feature via a mechanism that turns off Ethernet Connection Retention, so that the device either has the network connection retention behavior described in 7.3.1, or the device never attempts to retain network connections. In the latter case, it is like having no connection preservation so that connections are all reset as soon as the connection monitoring detects a disconnect.

7.4 Recommendation – Incorporate Auto-MDIX

LXI devices should incorporate Auto-MDIX.

7.5 RULE – Label Required on Modules Without Auto-MDIX

If Auto-MDIX is not used, the module shall be clearly labeled with a physical, human-readable label. A “soft” label, on an instrument display, for instance is insufficient.

7.6 RULE – Enable Auto-Negotiation by Default

Modules should support auto-negotiation by default to select the highest operating mode. In most cases Auto-Negotiation eliminates the need for the user to explicitly set the operating modes at both ends of the cable. Most Ethernet products enable Auto-Negotiation by default.

7.6.1 Recommendation – Provide Override for Auto-Negotiation

Modules should also provide a way for the user to override Auto-Negotiation for those (rare) situations when the results of Auto-Negotiation may not be what the user wants. Circumstances might include having 100BT capable nodes connected with CAT 3 (not capable of 100 Mbits/sec) cabling instead of CAT5. The auto-negotiate process in this case may select an operating mode that is too high for the installed cabling. For these reasons, it is recommended that modules allow the user to override Auto-Negotiation.
8 LAN Configuration

8.1 RULE – TCP/IP, UDP, IPv4 Network Protocols

LXI devices shall support TCP/IP networking, as outlined in a number of RFCs, including 791 (IP), 793 (TCP), and 768 (UDP). IPv4 shall be supported at a minimum.

LXI devices can be controlled and communicated with using any higher-level protocol (such as RPC), as long as it is built on top of the TCP or UDP transport layers.

Low-level protocols other than TCP/IP may be used for non-control applications.

8.1.1 Recommendation – IPv6

LXI devices should support IPv6 to ensure long-term network compatibility.

8.2 RULE – ICMP Ping Responder

LXI devices shall support ICMP (Internet Control Message Protocol, used for a Ping Responder) for diagnostics.

The TCP/IP stack shall be able to respond to the ICMP echo message used by the ping command. The ‘ping <hostname>’ or ‘ping <IP address>’ command is the standard way to understand whether a user’s connection to an Ethernet device is working.

8.3 RULE – ICMP Ping Responder Enabled by Default

ICMP Ping service (“Ping Responder”) shall be enabled by default.

8.4 Recommendation – Provide Way to Disable ICMP Ping Responder

It is recommended that the user have a way to disable the ICMP Ping Responder.

8.5 Recommendation – Support ICMP Ping Client

Modules should support ICMP Ping Client capability so that the user can ping other Ethernet devices.

8.6 DEPRECATED Recommendation - LAN Configuration Using ARP and ICMP Ping Responder

LAN Configuration using ARP and ICMP Ping responder has been deprecated for the 1.1 release of LXI. It will be eliminated in a future version. LXI devices are permitted to implement this LAN configuration technique, but the LXI Consortium is not actively recommending it.
LXI devices should be configurable using ARP and ICMP Ping Client according to the following procedure:

1. In a PC on the local subnet, set a routing path to the LXI device:
   `arp –s <new IP address> <MAC address>`

2. Notify the LXI device of the `<new IP address>` with an ICMP Ping packet:
   `ping <new IP address> [-l 113]`

   Note: This is a custom ICMP Ping Responder—it is responsible for setting the IP address into the LAN interface as well as setting the Web Password for configuration back to its factory default.

8.7 RULE – IP Address Configuration Techniques

LXI Modules shall support three LAN configuration techniques: DHCP, Dynamically Configured Link Local Addressing (Auto-IP), and manual. LAN configuration refers to the mechanism that the device uses to obtain IP Address, Subnet Mask, Default Gateway IP Address, and DNS Server IP Address(es).

Collectively, DHCP and Dynamically Configured Link Local Addressing are considered automatic configuration methods. These automatic methods may provide additional or supplemental user entries for DNS servers as appropriate. The DHCP and Manual configuration methods provide configuration for: 1) module IP address, 2) Subnet Mask, 3) Default Gateway IP Address, 4) DNS server IP addresses.
## Applicability of Configuration Methods to Network Topologies

<table>
<thead>
<tr>
<th>Network Topology</th>
<th>Automatic IP Configuration Methods</th>
<th>Manual IP Configuration Methods</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>DHCP</strong></td>
<td>Works on nearly all site/enterprise LANs because they are built with DHCP</td>
<td>Works in all network topologies</td>
</tr>
<tr>
<td><strong>Dynamic Link-Local Addressing</strong></td>
<td>Auto-IP not likely to be used here</td>
<td>Works in all network topologies</td>
</tr>
<tr>
<td><strong>Manual IP Address Configuration</strong></td>
<td></td>
<td>Works in all network topologies</td>
</tr>
</tbody>
</table>

- **DHCP**
  - Works on network built with Ethernet router with integrated DHCP server (or equivalent)
  - Auto-IP not likely to be used here

- **Manual IP Address Configuration**
  - Works in all network topologies

- **Desktop**
  - DHCP not likely to be used here
  - Works on 2-node network built with a crossover cable (no DHCP)
  - Works in all network topologies

---

DHCP current specifications RFC 2131 and RFC 2132 are found at:

8.7.1 RULE – Options for LAN configuration

LXI Modules shall support one of the following options for LAN configuration:

A single configuration setting of Automatic (implying DHCP and Dynamically Configured Link Local Addressing) or Manual.

Individual configuration settings for: DHCP, Dynamically Configured Link Local Addressing, and Manual. If more than one is enabled, the module’s LAN configuration shall proceed in the following order: 1) DHCP, 2) Dynamically Configured Link Local Addressing, 3) manual.

8.7.2 Recommendation – 30-Second DHCP Timeout

Modules should implement a 30-second DHCP time-out to control how long the DHCP client will wait for a response from a DHCP server before giving up.

8.7.3 RULE – Explicitly Request All Desired DHCP Parameters

LXI devices shall explicitly request all desired DHCP parameters from the DHCP server. A DHCP client uses the “parameter request list” option to request specific parameter values from a server. The LXI device DHCP implementation should ensure that parameters like default gateway and subnet mask are in the “parameter request list.”

8.7.4 Recommendation – Accept the First DHCP Offer Received

Modules should accept the first DHCP OFFER message received.

The DHCP protocol specifies that a DHCP client emit a DHCP discovery message to find a DHCP server, then wait for DHCP offer messages from DHCP servers. The protocol allows, but does not require, the client to collect multiple offers prior to requesting an address from one of the responding servers. Some DHCP implementations accept multiple offers, but none allows the user to select which DHCP server is used. Accepting the first DHCP OFFER is the most common implementation and produces the fastest IP configuration via DHCP.

8.7.5 RULE – Do Not Require Additional DHCP Options for Normal Operations

Modules shall not require any additional DHCP options for normal operations beyond what is needed for IP and DNS configuration. Other options may be requested, but the operation of the module shall not depend on receiving these parameters.

8.7.5.1 Permission – Additional DHCP Options Allowed for Module Updates

Network boot support, which requires an additional DHCP option, may be used to update modules.

8.7.6 RULE – Stop Using IP Address If DHCP Lease Not Renewed

If a module is unable to renew its DHCP lease it shall stop using the DHCP supplied IP configuration that failed to be renewed and, if so equipped, offer an alarm or error message.
8.7.7 RULE – Honor New DHCP Options at Lease Renewal

LXI devices shall honor new DHCP options provided when renewing a lease.

8.7.8 Recommendation – Provide Manual DNS IP Address Entry

Modules should allow the user to enter DNS server(s) IP addresses. The automatic IP configuration with manual DNS configuration enables the user to select a specific DNS configuration in addition to the DHCP configuration information. This is useful in network environments with a DNS server per department and a DHCP server per site.

8.7.9 Permission – User Configured Hosts File Allowed

Modules may support a user configured hosts file.

Some modules that will have users running many network client applications (web browsing, etc) directly on the module may want to support the ability to set up a hosts file. A hosts file is a manual way for the user to set up specific mappings between hostnames and IP addresses.

8.8 RULE – Duplicate IP Address Detection

Modules shall perform duplicate IP address detection to ensure an LXI device does not start using an IP address that is already in use on that network.

Modules shall disconnect from the network when a duplicate IP address is detected.

8.9 Recommendation – Check Network Configuration Values for Validity

The values entered by the module user should be checked to ensure they are in the valid range.

8.10 Recommendation – Single Hostname for All Naming Services

LXI devices should have a single module default hostname used for all dynamic naming services. The single module hostname shall be a legal DNS name.

Default Hostname recommendations:
- Syntax requirements:
- Maximum length of 15 characters.
- First character must be a letter (RFC 1035).
- Last character must be either a letter or a digit (RFC 1035).
- Intervening characters must be either a letter or a digit or a hyphen (RFC 1035).

Within a subnet or system or DNS domain, this name needs to be unique. Therefore, a pattern constructed from the model name and last part of the serial number should normally meet this requirement, as in the following example from Agilent Technologies: A-E4440A-12345.
8.11 RULE – Provide an Error Indicator for LAN Configuration Faults

LXI devices shall make use of the LXI LAN Status Indicator to inform the user of a LAN fault or error caused by:
- failure to acquire a valid IP address
- detection of a duplicate IP address
- failure to renew an already acquired DHCP lease (failure to obtain an initial DHCP lease is not a failure)
- LAN cable disconnected (as reported by Ethernet connection monitoring)

See 2.8.2 LAN Status Indicator for annunciation details.

The LXI LAN Status indicator indicates both the LAN error conditions above and provides an identify indication initiated by the user via the Web interface or API as described in section 2.8.2. The LXI LAN Status indicator shall provide LAN Fault, Normal Operation, and Device Identify indications as shown in the state diagram below. Note that the state labeled “State Undefined” is transitory and the behavior of the indicator is not specified.

Regarding DHCP lease renewal failure and Auto-IP, there are two cases to consider. In both cases the instrument is configured to automatically obtain an IP Address (with both DHCP and Auto-IP on). In the first case, when the device is connected to the network, it fails to obtain an IP Address through DHCP, and therefore claims an Auto-IP address. When this happens, the LAN Status Indicator should indicate no fault.

In the second case, when the device is connected to the network, it does successfully obtain a DHCP lease. However, at a later time the device fails to renew that lease through DHCP. Per rule 8.7.6 the device must stop using the IP Address it had obtained through DHCP at this point and the LAN Status Indicator must indicate a fault. Now, since Auto-IP is configured the device will then obtain an Auto-IP address. Despite the fact that the device now has an Auto-IP address, the LAN Status Indicator must remain in the fault state. This is to indicate to the user that a DHCP lease renewal has failed and that the device does not have the same IP Address that it did before.

At this point, the LAN Status Indicator must remain in the fault state until one of the following happens. 1) The device successfully acquires a new DHCP lease. (This can happen if it is configured to periodically attempt to obtain a new DHCP lease.). 2) The device is restarted. 3) The LAN Configuration is reinitialized for the device by the user. (This could be done through the LCI, unplugging and replugging the LAN cable, or another mechanism if the device is so equipped.) In scenarios 2 and 3, the behavior when the device again attempts to obtain an address is the same as in the first case, if DHCP fails but an Auto-IP address is obtained, the LAN Status is no fault.
8.12 **Recommendation – Support Dynamic DNS Hostname Registration**

LXI devices should support hostname registration through DHCP servers with cooperating Dynamic DNS servers.
8.12.1 Recommendation – Provide User Control of Dynamic DNS Registration

Modules should allow the user to turn the Dynamic DNS capability on or off. On networks without Dynamic DNS support, the network ignores the hostname request sent out by the module. Some users may want to disable Dynamic DNS at the module to make use of a default hostname assigned by the network.

8.12.1.1 RULE – If Dynamic DNS Can Be Disabled, Its Default State Is Enabled

Modules that allow Dynamic DNS to be turned off shall have the Dynamic DNS capability enabled by default

8.13 RULE – Actual Hostname Display

LXI devices shall display the actual module hostname to the user through the module user interface (web page).

8.13.1 Recommendation - Provide DNS Client

Modules should support a DNS client for resolving hostnames.

8.13.2 Recommendation – How To Determine Actual Hostname

Modules should use the following algorithm to determine module hostname; it covers all the conditions described above, and allows the module to determine its hostname. The algorithm is:

1. If there is a DNS server address configured in the module (either via DHCP or manually configured), do the following:
   a) Determine the module’s IP address (DHCP/Manual/Dynamic Link-Local Addressing).
   b) Do a reverse DNS look-up (IP address to hostname lookup) to determine the module’s hostname on the network.
   c) If the lookup fails, then go to step 2.
   d) Do a forward DNS look-up (hostname name to IP address lookup) to validate that the hostname can be resolved, and the same IP address is returned.
   e) If the lookup fails, then go to step 2.
   f) If the IP address from step 1a and 1c are different, then there is something wrong with the DNS hostname configuration. Proceed to step 2.
   g) The hostname determined in step 1c is the correct hostname, and this hostname can be presented through the appropriate places in the module’s user interface.

2. There is no hostname assigned to this module.
   a) Use the IP address in place of an actual hostname, and the IP address can be presented through the module user interface.

8.13.3 Rule – Hostname Display

If a module does not support recommendations 8.13.1 and 8.13.2 then it shall show the assigned IP address or a blank field for the hostname.
8.14 RULE – LAN Configuration Initialize (LCI)

LXI Devices shall provide a LCI reset mechanism, as defined in 2.7.5, that when activated places the module's network settings to a default state. These settings shall take effect when the LCI mechanism is activated, without requiring any further operator actions (e.g., if the module requires a reboot for the changes to take effect, the module shall reboot automatically). The module default state shall be fully documented and available in the manufacturer’s supplied documentation.

<table>
<thead>
<tr>
<th>Item</th>
<th>Value</th>
<th>Section</th>
</tr>
</thead>
<tbody>
<tr>
<td>IP Address Configuration:</td>
<td></td>
<td></td>
</tr>
<tr>
<td>DHCP</td>
<td>Enabled</td>
<td>0</td>
</tr>
<tr>
<td>ICMP Ping Responder</td>
<td>Enabled</td>
<td>8.3</td>
</tr>
<tr>
<td>Web Password for configuration</td>
<td>Factory Default</td>
<td>9.8</td>
</tr>
<tr>
<td>Dynamic DNS (if implemented)</td>
<td>Enabled</td>
<td>8.12.1.1</td>
</tr>
<tr>
<td>mDNS and DNS-SD</td>
<td>Enabled</td>
<td>10.3, 10.4, 10.7.1</td>
</tr>
</tbody>
</table>

Note: mDNS and DNS-SD are covered by FUTURE Rules in this version of the standard. As they are not required for this version of the standard, modules that do not implement them do not need to implement the LCI behavior either.

If a module has a manual user interface (physical front panel) that allows the configuration of these items plus the network configuration, then that shall be sufficient to meet the needs addressed by this button, as long as there is a single LAN Configuration Initialize key in the manual interface that sets the items in the above table as indicated.

8.14.1 Recommendation – LAN Configuration Initialize (LCI) Additional Settings

In addition to the settings listed in 8.14, The LCI mechanism should enable dynamically configured link local addressing, disable manual IP, and enable auto-negotiation.
9 Web Interface

9.1 RULE – Web Pages Using W3C Compliant Browsers

LXI devices shall serve a HTML web page that works correctly with all W3C compliant browsers. LXI device web servers shall conform to HTTP (version 1.0 or greater). The HTML pages served shall conform to HTML (version 4.01 or greater) or XHTML (version 1.0 or greater).

9.1.1 RULE – Protocol and Port Number

LXI devices shall accept HTTP connections on port 80 and serve the LXI required welcome page as a response to such connection requests. Navigation buttons or hyper links are allowed to access other ports as desired by the web page authors.

9.1.2 Recommendation – Web Server Root Document

The LXI device should serve a web page from the root document set with file name index.htm or index.html so that the URL to access a module is http://<host>, where <host> is either a hostname or IP address. Also, the LXI device web server should be configured to automatically return the file index.htm or index.html by default.

9.2 RULE – Welcome Web Page Display Items

The primary LXI welcome page shall display the following information in a read-only format.

- Instrument Model
- Manufacturer
- Serial Number
- Description
- LXI Class <A, B, or C>
- LXI version (initially 1.0, but it will grow)
- Hostname
- MAC Address <XX-XX-XX-XX-XX-XX>
- TCP/IP Address <DDD.DDD.DDD.DDD>
- Firmware and/or Software Revision
- IEEE 1588 PTP Current time [Optional for LXI class C instruments]

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4 This is a user configurable string that can be used to describe the instrument. For example it could describe the location of the instrument.

5 This is a user configurable string that describes the hostname for DNS purposes
9.2.1 RULE – Instrument Address String on Welcome Page

The primary LXI welcome page shall contain an IVI I/O Resource Descriptor, which is a string that specifies the address of the hardware asset that can be recognized by the I/O used by a software module that accesses the hardware. An example of such a Resource Descriptor is a VISA Resource.

For VISA Resources of the form

TCPIP[board]::host address[::LAN device name]::INSTR

or

TCPIP[board]::host address::port::SOCKET

the value of “[board]” must be empty since the instrument cannot know which interface board a client may be using.

9.2.1.1 Recommendation – Instrument Address String Label

This string should be labeled “Instrument Address String.”

9.2.2 Recommendation – Web Page Title

An LXI instrument web page title should follow the following format to align the bookmarks nicely:

LXI – Manufacturer-Model-<Optional Serial Number>-<Optional Description>

9.3 RULE – Device Identification Functionality on the Web Page

There shall be a device identification indicator functionality on the web page to control the LAN Status Indicator (see Section 2.8.2).

9.3.1 Permission – No password protection for device identification indicator

The device’s identification indicator functionality is not considered as an instrument setting. So the web page that exposes this functionality may not be password protected.

9.4 RULE – LAN and Sync Configuration Links on the Welcome Page

The Welcome page shall contain at least two hyperlinks/buttons to provide further information or to allow the user to configure module settings. The first linked web page shall contain the information as described in section 9.5 and the second linked web page shall contain the information as described in section 9.6. The second link (Synchronization web page contents) is applicable for LXI class A and B instruments.

9.4.1 Recommendation – Status Page Link on the Welcome Page

There should be an additional hyperlink/button – Status/Miscellaneous page on the LXI welcome page.
9.5 **RULE – LAN Configuration Web Page Contents**

The LAN configuration page shall contain the following parameters to configure the LAN settings:

- Hostname
- Description
- TCP/IP Configuration Mode
- IP address
- Subnet mask
- Default Gateway
- DNS Server(s)

The TCP/IP configuration field controls how the IP address for the instrument gets assigned. For the manual configuration mode, the static IP address, subnet mask, and default gateway are used to configure the LAN. The automatic configuration mode uses DHCP server or Dynamic Link Local Addressing (Automatic IP), as described in Rule 8.7 to obtain the instrument IP address.

9.5.1 **Recommendation – Default Description for LXI Instrument**

The default description for the LXI instrument should be manufacturer name, instrument type, model, and the serial number (e.g., Xyz Oscilloscope 54321D – 123456).

9.5.2 **Recommendation – Auto-Negotiate Enable/Disable Through Web Page**

If the LXI instrument implements auto-negotiate enable/disable, then it should be exposed through the web page.

9.5.3 **Recommendation – Ping Enable/Disable Through Web Page**

If the LXI instrument implements ping enable/disable, then it should be exposed through the web page.

9.5.4 **Permission – Other Information on the LAN Configuration Page**

Other additional information/IP configuration settings may be added to the IP configuration page (e.g., Domain Name).

9.5.5 **Permission – Disable Switch for LAN Configuration Page**

The IP configuration web interface may be disabled with a non-volatile switch or a key. For example, this switch may be a physical jumper setting or a front panel menu item in the instrument.

9.6 **RULE – Sync Configuration Web Page Contents**

This is a rule for LXI class A and B instruments and recommendations for LXI class C instruments.

---

6 User assigns an IP address to the instrument and is only used if the TCP/IP Configuration Mode is Manual.

7 For the instrument to be used by its hostname the instrument needs to register its IP address with a DNS server. The IP address of the DNS server is required for this purpose.

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The Synchronization Configuration page has information about synchronizing multiple LXI devices on the LAN. This is a rule for LXI class A instruments and the web page shall contain the IEEE 1588 and Wired trigger parameters as defined by the following table. This is also a rule for LXI class B instruments and the web page shall contain the following IEEE 1588 parameter as defined in the following table:

<table>
<thead>
<tr>
<th>Item</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>IEEE 1588 Parameters:</strong></td>
<td></td>
</tr>
<tr>
<td>Current grandmaster clock</td>
<td>Hostname, IP address, or MAC address</td>
</tr>
<tr>
<td>Parent clock</td>
<td>Hostname, IP address, or MAC address</td>
</tr>
<tr>
<td>State</td>
<td>Master, Slave, Faulty, Disabled, Passive, Uncalibrated, Other</td>
</tr>
<tr>
<td></td>
<td>(Initializing, Listening, Pre-master)</td>
</tr>
<tr>
<td>Current PTP time</td>
<td>Seconds since 0 hours, 1 January 1970 TAI (represented as a string of the form “seconds.fractional seconds”)</td>
</tr>
<tr>
<td>Current local time (if available)</td>
<td>Date/time</td>
</tr>
<tr>
<td>Current grandmaster traceability to UTC</td>
<td>GPS, NTP, HAND or ATOM… (Vendors can define others)</td>
</tr>
<tr>
<td>Current observed variance of parent</td>
<td>In nanoseconds²</td>
</tr>
<tr>
<td>clock</td>
<td></td>
</tr>
<tr>
<td>IEEE 1588 Domain</td>
<td>As defined by IEEE 1588 Standard</td>
</tr>
<tr>
<td><strong>LXI Module-to-Module Parameters:</strong></td>
<td></td>
</tr>
<tr>
<td>LXI Domain</td>
<td>As defined in Section 4</td>
</tr>
<tr>
<td><strong>Wired Trigger Parameters:</strong></td>
<td></td>
</tr>
<tr>
<td>Wired-Or Bias</td>
<td>Enabled or Disabled (default) for each of LXI0 to LXI7</td>
</tr>
</tbody>
</table>

**Note:** Depending on the implementation, the value of the “Current PTP time” can be obtained by (1) directly reading the IEEE 1588 clock and translating into the display format or (2) using the timestamp received in an IEEE 1588 management message with managementID = Time, and translating into the display format.

**Note:** Devices that do not compute the “Current observed variance of parent” parameter shall display “Unavailable” (without quotes) as the parameter value.

### 9.7 Recommendation – Status Web Page Contents

The status/miscellaneous page should contain the following information:

- Status
- Errors/Warnings

The status field should contain busy status with any armed/trigger waiting status and any instrument-specific status information. Dynamic updates for this page should not be necessary.
9.7.1 Permission – Other Information on the Status Web Page

Any other additional information may be added to the status/miscellaneous page (e.g., Instrument options).

9.8 RULE – Web Page Security

Any page(s) that allows user to change the instrument’s settings shall be password protected; user changeable default passwords are acceptable.

9.8.1 Permission – Blank password

The LXI device’s default password may be blank and the web interface may not need to put up a dialog box for a blank password.

9.9 RULE – LXI Logo

All the required web pages for an LXI instrument shall contain an LXI compliance logo (See section 14.6).

9.10 Recommendation – LXI Web Interface Example

LXI device web interfaces should follow the similar look and feel as the examples web pages in Appendix A.

9.11 Recommendation – LXI Instrument Control Using Web Page

LXI devices should provide the ability to interact, control, setup and perform troubleshooting on the most common functions through a web interface, without writing a program.

9.12 Recommendation – Software/Firmware Upgrade Using Web Interface

As needed, LXI devices should be able to update software/firmware utilizing the embedded Web interface. Updates should include minor file updates, major software updates, measurement application downloads, or OS changes.

9.13 Recommendation – LXI Glossary

LXI device web interface should support one of the following options to help explain the terminology used in this LXI specifications document:

- Copy of the glossary from the LXI specifications
- Link to a help file contains the glossary
- Link to a help file contains the glossary on the instrument vendor's home page

9.14 Recommendation – SNMP

LXI devices should support SNMP.
9.14.1 Recommendation – MIB Roadmap

Roadmap⁸: The specific MIB implemented by LXI should be defined. Recommend a standard MIB.

9.14.2 Recommendation – SNMP Not Suitable for Extracting Measurement Data

Limit SNMP to traditional "network management" functions, such as network configuration, network statistics gathering, and asset management. SNMP should not be suitable for extracting measurement data.

9.15 RULE – All URLs Beginning With “LXI” Are Reserved by the LXI Consortium

RFC 1738 defines the HTTP URL as the following:

http://<host>:<port>/<path>?<searchpart>

Any URL with a <path> that begins with the strings “lxii” or “LXI” or any combination of lowercase and uppercase letters combined to spell LXI are reserved for Consortium-defined uses. This includes the directory-like syntax in which the first part of <path> is any combination of lowercase and uppercase letters that spell LXI terminated with a “/”:

http://<host>:<port>/lxii/<path>?<searchpart>

⁸ Roadmap is available on the LXI Web Interface working group website.

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10 LAN Discovery and Identification

Roadmap: mDNS and DNS-SD will be requirements for the next version of the LXI standard. Items labeled “FUTURE RULE” cover behavior that is recommended but not required for this version of the standard. “FUTURE” will be dropped in the next version of the standard requiring the specified behavior.

Note: While the Consortium has made considerable effort in specifying and testing mDNS and DNS-SD as outlined below, it is possible that some rules and/or recommendations will be changed, dropped, or added in the next version of the standard to correct issues or improve functionality.

10.1 RULE – Support VXI-11 Discovery Protocol

The VXI-11 protocol shall be supported by all LXI devices for discovery purposes. Discovery shall be accomplished by issuing a broadcast RPC call on the host’s subnet. The broadcast RPC shall be to either the portmapper itself on port 111 (querying for VXI-11 support) or the NULL procedure (procedure 0) on the Program Number assigned to the VXI-11 Core Service (0x0607AF).

Note: At some point in the future, VXI-11 may no longer be required.

10.1.1 RULE – VXI-11 Servers Respond Within One Second

All VXI-11 servers shall respond to a broadcast RPC to the NULL procedure within 1 second.

10.1.2 RULE – SCPI *IDN?

At a minimum an LXI device shall be able to respond to the IEEE 488.2 “*IDN?” command. This is a simple query that returns four comma-separated fields, which indicate manufacturer, model, serial number, and firmware version⁹.

10.1.2.1 Permission – Additional VXI-11 and SCPI Support Is Optional

LXI devices may support additional VXI-11 functionality and SCPI commands beyond that required for discovery.

10.2 RULE – XML Identification Document URL


Please see Appendix C for example Identification Documents.

⁹ For more information see IEEE 488.2 Section 10.14.
10.2.1 Permission – HTTP Redirection

LXI devices may return an HTTP Status Code indicating Redirection – the 3xx range of values (e.g., 300, 301, 302, etc. of RFC 2616) – in response to a GET request on the URL defined in 10.2. Clients are expected to handle these redirections appropriately.

10.2.2 RULE – Content Type Header

The response to the GET request on the URL defined in 10.2 or to the URL that actually returns the XML document after possible redirection(s) shall include the “Content-Type” header with “text/xml” as the value.

10.2.3 RULE – Schema Location Attribute

The xsi:schemaLocation attribute of the root element of the identification document shall contain an entry for the LXI XSD namespace with an accompanying absolute URI on the instrument that shall return the actual XSD schema document from the instrument (http://www.w3.org/TR/xmlschema-0/#schemaLocation). The W3C XSD Schema itself (the “xsi” namespace of http://www.w3.org/2001/XMLSchema-instance) does not need to be available via a URI on the instrument.

Example:

```xml
<LXIDevice
   xmlns='http://www.lxistandard.org/InstrumentIdentification/1.0'
   xmlns:xsi='http://www.w3.org/2001/XMLSchema-instance'
   xsi:schemaLocation='http://www.lxistandard.org/InstrumentIdentification/1.0 http://1.2.3.4/identification.xsd'>
   <!-- other elements and attributes not shown here -->
</LXIDevice>
```

10.2.4 RULE – Connected Device URLs

Devices that support connected devices (e.g., bridges) shall provide base URLs for all connected devices in the ConnectedDevices element of the identification document. A base URL is defined as a URL with a “url-path” that clearly identifies the connected device and one onto which a suffix path may be added to access properties of that connected device. The base URL allows clients to enumerate devices connected to the bridge device.

For example, the base URL for a connected device might be “http://hostname/device0” while another connected device might have a base URL of “http://hostname/devices5”. The format and path naming conventions for these connected device base URLs are left up to the vendor.

The following is an example snippet from an identification document with connected device DeviceURI elements:

```xml
<ConnectedDevices>
   <DeviceURI>http://10.1.2.60/devicesLogicalAddress/0/</DeviceURI>
   <DeviceURI>http://10.1.2.60/devicesLogicalAddress/1/</DeviceURI>
</ConnectedDevices>
```

Future versions of this standard may implement additional web interfaces (e.g., resource management) that can be used on LXI devices as well as on connected devices.
10.2.4.1 RULE – Connected Device XML Identification Document URLs

Devices that support connected devices shall provide identification documents that can be queried via a GET on <baseURL>/lxid/identification that conform to the LXI XSD Schema or one derived from that Schema according to the rules of XSD inheritance. The <baseURL> values may be found in DeviceURI elements of the ConnectedDevice element of the root element of the identification document of Rule 10.2. This rule coupled with Rule 10.2.4 allows clients to enumerate (discover) and identify all connected devices.

A future version of this standard may require that ConnectedDevices for common buses (e.g., VXI, PXI, GPIB, etc.) use particular derived schemas published by the LXI Consortium.

10.2.4.2 RULE – Connected Device XML Identification Document Schema Location Attribute

The xsi:schemaLocation attribute of the root element of the identification document shall contain an entry for the LXI XSD namespace with an accompanying absolute URI on the instrument that shall return the actual XSD schema document from the instrument (http://www.w3.org/TR/xmlschema-0/#schemaLocation). The W3C XSD Schema itself (the “xsi” namespace of http://www.w3.org/2001/XMLSchema-instance) does not need to be available via a URI on the instrument.

10.3 FUTURE RULE – Support mDNS

LXI devices shall support Multicast DNS (mDNS) as defined by http://files.multicastdns.org/draft-cheshire-dnsextnet-multicastd.txt.

10.3.1 FUTURE RULE – Claiming Hostnames

Devices supporting mDNS shall assign themselves a link-local hostname and shall automatically resolve link-local hostname conflicts.

10.3.1.1 FUTURE RULE – Hostname Conflicts

If a link-local hostname conflict occurs, the LXI device shall assign itself a new hostname and retry until the conflict is resolved. New hostnames shall be generated by appending a number to the end of the hostname. For example, a conflict on “Instr-ABC” would become “Instr-ABC-2” after the first collision, “Instr-ABC-3” on the second, and so on.

10.3.2 Recommendation – Default Link-Local Hostname

The default hostname as defined in section 8.10 should be used as the default link-local hostname.

10.3.3 FUTURE RULE – Dynamic DNS Update and mDNS Hostname

LXI devices that support Dynamic DNS Update shall use the user-configured hostname as the link-local hostname.
10.3.4 FUTURE RULE – DHCP “Host Name” Option and mDNS Hostname

If an LXI device supports the DHCP “Host Name” option (option code 12), that LXI device shall not use the DHCP-provided hostname as the link-local hostname, but rather the default hostname or the valid hostname. (See Section 10.7.)

Observation: This rule helps ensure stability since the link-local hostname should remain unchanged when the device is moved between different networks.

10.4 FUTURE RULE – Support DNS-SD

Devices shall support DNS Service Discovery (DNS-SD) as defined by http://files.dns-sd.org/draft-cheshire-dnsextdns-sd.txt via mDNS and traditional unicast DNS.

10.4.1 FUTURE RULE – Claiming Name Service

LXI devices shall assign themselves a service name used to advertise services defined within this standard and shall automatically resolve service name conflicts.

10.4.2 FUTURE RULE – Single Service Instance Name for LXI Defined Services

LXI devices shall assign themselves a single service name for use in advertising all required and recommended LXI services, as below, and shall resolve service name conflicts. The service name is the “instance” portion of a service name as follows:

<instance>, <service>, <domain>

Thus an HTTP service with an instance name of “Instrument ABC” in the “.local” domain will have “Instrument ABC._http._tcp.local” as the service name.

10.4.2.1 FUTURE RULE – User Configurable Service Name

LXI devices shall allow a user to modify the non-volatile service name via the web interface, truncated to the first 63 bytes of UTF-8. When a user modifies a service name, the LXI device shall unregister all services and then reregister using the new service name.

10.4.2.2 FUTURE RULE – Default Service Name

LXI devices shall use the recommended default description of section 9.5.1 for their default service name truncated to the first 63 bytes of UTF-8.

10.4.2.3 FUTURE RULE – Service Name Conflicts

If a link-local service name conflict occurs, the LXI device shall assign itself a new service name and retry until the conflict is resolved. New service names shall be generated by appending a number to the end of the service name. For example, a conflict on “Vendor Instrument” would become “Vendor Instrument (2)” after the first collision, “Vendor Instrument (3)” on the second, and so on.
10.4.3 FUTURE Rule - Required Service Advertisements and TXT Record Keys

Devices that support DNS-SD shall, at a minimum, advertise the following services via mDNS and shall provide the related keys in the TXT records for the service. Please refer to 10.4.3.5 for Permission on TXT Record Keys with default values.

<table>
<thead>
<tr>
<th>Service Type</th>
<th>TXT Record Keys - Required</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>http (_http._tcp)</td>
<td>txtvers=&lt;version of TXT record&gt;; default &quot;txtvers=1&quot;; current version is 1 path=&lt;path to the root or index page of the server&gt;; default &quot;path=&quot;/</td>
<td>All HTTP servers that a device supports that may be used with a typical web browser</td>
</tr>
<tr>
<td>lixi (_lixi._tcp)</td>
<td>txtvers=&lt;version of TXT record&gt;; default &quot;txtvers=1&quot;; current version is 1 Manufacturer=&lt;first element of response to IEEE 488.2 *IDN?&gt; Model=&lt;second element of response to IEEE 488.2 *IDN?&gt; SerialNumber=&lt;third element of response to IEEE 488.2 *IDN?&gt; FirmwareVersion=&lt;fourth element of response to IEEE 488.2 *IDN?&gt;</td>
<td>An LXI service that uses the HTTP protocol for identification and other operations as defined by this standard</td>
</tr>
</tbody>
</table>

10.4.3.1 FUTURE RULE – TXT Records Are Required

The LXI device shall provide a TXT record for every service instance being advertised. If there are no TXT record entries for a service (see Permission 10.4.3.5), an empty TXT record shall be provided.

10.4.3.2 FUTURE RULE – TXT Records Consist of Key/Value Pairs

TXT records shall consist of key/value pairs of the form “name=value” (without quotes). The value begins after the first ASCII equal sign “=” and continues to the end of the string. The maximum length of a key/value pair is 255 bytes.

10.4.3.3 FUTURE RULE – TXT Record Keys Are Case-Insensitive ASCII

All TXT record keys (names) shall be printable ASCII characters (0x20-0x7E), excluding “=” (0x3D), and shall be case-insensitive.
10.4.3.4 FUTURE RULE – TXT Record Values

TXT record values (data beginning after the ASCII equal sign “=” [0x3D]) in general shall be opaque binary data, but may be ASCII or UTF-8 for particular keys.

10.4.3.5 Permission – TXT Record Key Default Values

If the value of a TXT record key is equal to the default value for that key, it may be omitted from the TXT record.

10.4.3.6 FUTURE RULE – TXT Record Key Order

For any service that has a defined TXT record key of “txtvers” the “txtvers” key, if present, shall be the first key in the TXT record.

10.4.3.7 FUTURE RULE – LXI Consortium TXT Record Keys

All TXT record keys beginning with “LXI” or “lxi” are reserved for Consortium-defined usage.

10.4.3.8 FUTURE RULE – Vendor Defined TXT Record Keys

All TXT record keys (names) used with LXI Consortium required or recommended services shall be either keys (names) as defined by this standard or vendor-specific keys. Vendor-specific keys shall end with the vendor’s domain name in accordance with section 6.4 of http://files.dns-sd.org/draft-cheshire-dnsextdns-sd.txt. That is, vendor-defined keys shall be of the form “keyname.company.com=.”

10.4.3.9 Recommendation – Maximum Length of TXT Record

TXT records should be no longer than 512 bytes.

10.4.3.10 Recommendation – Additional Service Advertisements

If LXI devices support the following services, they should advertise the services via mDNS:

<table>
<thead>
<tr>
<th>Service Type</th>
<th>TXT Record Keys</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>scpi-raw</td>
<td>txtvers=&lt;version of TXT record“&gt;; default &quot;txtvers=1&quot;; current version is 1</td>
<td>Raw SCPI (IEEE 488.2) command interpreter</td>
</tr>
<tr>
<td></td>
<td>Manufacturer=&lt;first element of response to IEEE 488.2 *IDN?&gt;</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Model=&lt;second element of response to IEEE 488.2 *IDN?&gt;</td>
<td></td>
</tr>
<tr>
<td></td>
<td>SerialNumber=&lt;third element of response to IEEE 488.2 *IDN?&gt;</td>
<td></td>
</tr>
<tr>
<td></td>
<td>FirmwareVersion=&lt;fourth element of response to IEEE 488.2 *IDN?&gt;</td>
<td></td>
</tr>
</tbody>
</table>
Telnet server supporting SCPI (IEEE 488.2) commands

VXI-11 Server

Note: Devices should advertise the VXI-11 service only if they support a complete and useful VXI-11 implementation (e.g., full command interpreter for the device). Devices with only minimally conformant VXI-11 services, as required in section 10.1 for discovery, are discouraged from advertising their VXI-11 service.

10.5 FUTURE RULE – mDNS and DNS-SD Enabled by Default

Both mDNS and DNS-SD shall be enabled by default on LXI devices.

10.5.1 FUTURE RULE – mDNS and DNS-SD Enabled by LAN Configuration Initialize (LCI)

When the LCI reset mechanism is activated, it shall enable mDNS and DNS-SD.

10.6 FUTURE RULE – mDNS Name Resolution

Devices that support mDNS shall use mDNS for name resolution. If, in addition, the device is configured to use traditional unicast DNS (e.g., by static or DHCP configuration), mDNS shall have higher precedence in name resolution.
10.7 FUTURE RULE – Nonvolatile Hostnames and Service Names

To promote stability, if a hostname conflict occurs and the LXI device chooses a new hostname, the device shall save the new hostname in nonvolatile storage for use the next time the device is powered on. Similarly, if a service name conflict occurs and the LXI device chooses a new service name, it shall save the new service name in nonvolatile storage for use the next time the device is powered on.

10.7.1 FUTURE RULE – Hostname and Service Name Revert to Default

When the LCI mechanism is activated, the hostname and the service name shall revert to the last user-configured values, if available, or factory defaults otherwise.

10.8 FUTURE RULE – Link Changes

When a network “link change” occurs (e.g., an Ethernet cable is plugged in), the LXI device shall verify that its hostname and service name are unique and shall re-register its services.
11 Security

There are two scenarios where LXI devices might be applied:

- In a test system rack, where all module connections are within the rack or racks. This use case is most easily secured with the use of an appropriate router to limit access from the outside. This is expected to be the majority use case.

- As an individual module on the network. In this case more protection may be required inside the module, including possibly firewalls, virus scanners, port closures, virtual private network connections, etc. We need more input from the user community to determine what is needed for this use case.

*Other security items for the roadmap:*

- Military secure erase
- Virus scanning appropriate to the module OS used
12 Documentation

12.1 RULE – Full Documentation on IVI Interface

For each LXI Device, the module manufacturer shall provide the documentation on the IVI driver, which is required in the IVI 3.1 Driver Architecture Specification, Section 5.21 Compliance Documentation.

12.2 RULE – Registration of the IVI Driver

The IVI driver shall be registered at the IVI Foundation website and be listed on the IVI Foundation driver registration database.

12.3 Recommendation – Documentation on Module Web Page

The documentation should be provided through the instrument/module webpage or accessible from the vendor website.
13 LXI Licensing

LXI is a trademark of the LXI Consortium Inc., which reserves the right to allow or disallow use of
the LXI label on products and published material based on conformance to the LXI Standards.

13.1 RULE – Trademark Only Available to Members in Good
Standing

Only LXI Consortium members in good standing, as described in the LXI Consortium Bylaws, and
its licensees may use the LXI Trademark.

13.2 RULE – Devices Must Comply with All LXI Rules To Use
Trademark

LXI Devices shall meet the rules listed in this document to qualify for using the LXI trademark.

13.3 RULE – Permitted Use of the Trademark

Use of the LXI Trademark shall comply with the following:

1. With regard to any logo version of the LXI Trademark, a member will only use
   artwork provided by the Consortium for the LXI Trademark, and will not distort,
   modify, or animate the LXI Trademark.
2. A member shall not use the LXI Trademark in combination with, nor include the
   LXI Trademark in, any other name, word, or Trademark, including the Company’s
   corporate name, business name, or domain names.
3. Upon request by the Consortium, a member shall submit to the Consortium samples
   of all marcom, packaging, and product bearing the LXI Trademark for approval, and
   shall make whatever changes to the display of the LXI Trademark that the
   Consortium requests. After samples have been approved pursuant to this paragraph,
   the Company shall not depart there from in any material respect without prior written
   consent by the Consortium. Approval by the Consortium does not constitute a
   waiver of any of Consortium rights, or of any of the Company’s duties under this
   license agreement. Items shall not be deemed approved unless and until approved by
   the Consortium in writing.

13.4 RULE – Logo Shall Conform To Design Guidelines

The LXI Logo shall conform to the design guidelines defined on the LXI Consortium Marketing
website. (www.lxistandard.org)

13.5 RULE – Trademark Use

Member or licensee agrees that all Products offered in connection with the LXI Trademark will
conform to the LXI Standard Specifications as set forth in this document.

Member or licensee agrees that the Products bearing or related to the LXI Trademark shall comply
with all applicable laws and regulations in connection with such Products.
Member’s or licensee's use of the LXI Trademark shall be in a manner consistent with the high standards, reputation, and prestige of the Consortium.

13.6 RULE – The License To Use

The license to use the LXI specification and logo shall expire 12 months from license grant, effective on the date of LXI Consortium dues expiration.

13.7 Trademark License

The Trademark License Agreement is publicly available at the LXI Consortium website.

13.8 RULE - Comply with IP Patent Policy

Any LXI Consortium member and Licensee shall comply with the LXI IP patent policy.
14 Conformance Specifications

14.1 Introduction

The LXI Conformance Specifications define the general rules and agreements under which equipment manufacturers shall apply and be granted permission to label their equipment with the LXI Logo, use the LXI logo in marketing literature, and advertise their equipment as LXI conformant.

Regulatory requirements and governmental type approval requirements are outside the scope of the LXI Conformance specifications. It is the sole responsibility of the vendor of a LXI Device to fulfill the different national regulatory requirements before product launch and use.

Passing the LXI Conformance Process demonstrates a certain measure of conformance and interoperability, but products are not tested for every aspect of the LXI specifications. The vendor has the ultimate responsibility to ensure that the LXI Device complies with the LXI Specifications and interopes with other devices.

14.2 General Intent of the Conformance Specifications

The Conformance Specifications are designed to ensure that the “Interoperability” goal of the LXI Consortium is met to the fullest extent possible while still allowing manufacturers to bring devices to market with the minimum of overhead in both time and cost. To this end, the specifications define, and manufacturers are encouraged to follow a “self regulation” policy to the greatest extent possible. The three methods under which a manufacturer can seek approval for a new LXI Device are:

- By testing it for interoperability against LXI Devices from other manufacturers in a controlled environment and using procedures approved by the Consortium.
- By applying for approval based on a written Technical Justification that it has a direct legacy from and traceability to an existing LXI Device that has already received approval.
- By certification from an independent Test Laboratory approved by the Consortium.

14.3 General Conformance Process

As with most standards, vendors are responsible for testing their devices against the LXI standard and documenting their conformance to the standard. A spreadsheet outlining each Rule is available from the LXI Consortium website.

To gain certification and licensing, vendors must submit conformance documentation and a device application, available from the LXI website, to the Conformance Working Group for approval. If complete, the Conformance Working Group then recommends certification to the LXI Board of Directors for approval.

Plug-fests are an important step toward improving interoperability across vendors by ensuring consistent spec interpretation and implementation. They also help identify problems that may be masked by a single vendor’s consistent implementation errors. Plug-fests offer a collaborative, supportive environment to help vendors screen and improve their LXI implementations.

The major steps in the conformance process are:
14.3.1 Prior to Plug fest

14.3.1.1 Vendor performs in-house testing

Vendor performs in-house testing to ensure conformance screening is likely to be successful.

In addition to the vendor's established Quality Assurance process the LXI Conformance spreadsheet can be used as a guideline to test against the rules according to the LXI Class to which the device will be conformant. Also available from the LXI Consortium Technical Working Groups are test procedures to test the rules of the different sections (e.g. LAN, Web Interface, Hardware Trigger, etc.) of the LXI standard.

14.3.1.2 Vendor pre-registers device for screening at the plug fest.

This is not required, but devices that are not pre-registered are screened only after all pre-registered devices have been screened.

- Download Plug fest pre-registration form from LXI Consortium Web site
- Fill out form with vendor and device information and areas to be tested
- Email form to LXI Conformance WG prior to plug fest

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14.3.2 At a Plug fest (public or privately arranged)

Vendor submits sample of device for screening. Also the vendor must provide a representative familiar with the operation of the device to assist consortium representatives with the screening.

DUT together with LXI Conformance spread sheet filled out with the appropriate information about DUT (including vendor declarations).

Each of the applicable TWGs executes their test plan in turn on the device, recording the outcome of each screening test in the according worksheet of the LXI Conformance spread sheet of the LXI Device under test.
All screening test results are collected during the different tests in the LXI Conformance spread sheet of the LXI Device under test.

The compiled test results are forwarded to the chair of the LXI Conformance WG at the conclusion of the plug fest.

14.3.3 After a Plug fest

Within one week of the conclusion of a plug fest, the Conformance WG will collate the results from each TWG for each device, and provide the vendor with an official record of the test results.

The record will include a summary statement identifying the device as “Fully Conformant”, “Conditionally Conformant”, or “Not Conformant” based on the requirements for the Class of Device for which the vendor requested testing. The record will also include the pass/fail status for each of the rules screened as part of the plug fest, and for any failures will provide details as to the nature of the failure. If the summary is “Conditionally Conformant”, the record will also include a statement explaining why conformance is conditional and what steps the vendor must take to achieve conformance.
14.3.4 Application for LXI Conformance

A vendor may submit to the LXI Consortium an “Application for LXI Conformance Certification”. The application affirms the vendor’s intent to comply with the LXI standard and to follow the rules of the consortium, include those for LXI copyright use and including those for the grievance resolution process. The application must be accompanied by:

- The LXI Conformance Documentation form including certification statement and general documentation about the LXI Device, vendor, contact and release date. The form is available from the LXI Conformance WG web site.

- The LXI Conformance Spread sheet with the official screening test results from a plug fest where the device was tested showing the device as “Fully Conformant” or “Conditionally Conformant”. For the latter, the application must also include a statement describing how/when the conditional requirement was satisfied.

The application should be sent to the Conformance WG. After successful inspection of the submitted information the Conformance WG chair will forward the application to the LXI Board of Directors with recommendation for approval as LXI conformant to the according Class A/B/C.
The application must be accompanied by any applicable license fee (applicable to non-members seeking licensing)

Within two weeks the LXI Consortium will notify the vendor that the application has either been approved or denied. If approved, the notification will include an “Official Certification of Conformance” that will detail the vendors rights to use the LXI trademark and advertise their device as LXI Conformant. If denied, the notification will explain why and detail what steps the vendor can take to resolve the issue(s). If an application is denied, the LXI Consortium will, upon request, refund the vendor’s license fee for that device.

14.3.5 Application for LXI Conformance on Technical Grounds

Manufacturers can use LXI legacy to claim compliance. If a family of devices uses a common LXI interface the passing of one device type can be used by the vendor to claim for the others, but the claim must still go through the process.

In lieu of the process described above, the application may include an “Application for LXI Conformance Certification on Technical Grounds”.

14.4 Conformance Grievance Process

The LXI Consortium has established a grievance process for resolving conformance, compatibility, and interoperability issues. This process

1. Provides a way for companies or individuals to raise concerns regarding illegitimate claims of LXI conformance,
2. Provides a way for the LXI Consortium to evaluate those complaints in a timely and equitable fashion, and
3. Establishes how the LXI Consortium responds to both legitimate and false complaints regarding conformance of LXI Devices.

14.4.1 Raising Concerns

To raise concerns about LXI conformance, a company, or individual should inform the LXI Consortium in writing of the complaint. The paper letter or email should be sent to the LXI Consortium business address. This letter must include:

- The LXI Device manufacturer name
- The LXI Device name (model, description, etc.)
- The software/firmware version of the LXI Device
- Any compliance claims made about the LXI Device
- Description of non-conforming behaviour

14.4.2 LXI Consortium Evaluation

When the LXI Consortium receives a complaint regarding conformance of a LXI Device, it will immediately notify the company that provides the LXI Device of the complaint. Within 30 days, the company must respond to the LXI Consortium. If the company does not respond, it is assumed to be claiming that there is no infraction and the arbitration process will be started.

If the company confirms the infraction, it will be given six months (in addition to the remainder of the 30 day response period) to either correct the specific flaws in the LXI Device or remove claims of LXI compliance. If the situation is not corrected within six months, the LXI Consortium will begin the censure process.

If the company claims the LXI Device is conformant, the arbitration process is initiated.

14.4.3 Arbitration

This process is invoked when there is a dispute regarding the validity of a complaint regarding the conformance of an LXI Device. It is presumed at the outset of this process that a written complaint as described above is available, as well as a written document from the provider of the LXI Device stating why it disputes the complaint.

To resolve the complaint, a Conformance Review Committee, chartered as a subcommittee of the LXI Technical Committee, will review and comment on claims. The Technical Committee chairman is responsible for creating the Conformance Review Committee and ensuring that all members of the Technical Committee have an opportunity to volunteer for the Conformance Review Committee. The Technical Committee chairman will initiate this process as soon as is convenient after being notified of the dispute. The membership will be made up of volunteer members from the Technical Committee; they shall elect an impartial chair from their membership. The committee may include both the LXI Device manufacturer and/or the person or company that initiated the complaint regarding the LXI Device in question.

The Conformance Review Committee will review the complaint. They will discuss the problem either in person or via phone meeting with the LXI Device manufacturer. The Conformance Review Committee will then formulate an authoritative opinion regarding the facts of the matter. The committee shall create a document either stating that the LXI Device appears to be conformant or stating the specific problems with the LXI Device, including references to the appropriate LXI
specifications as to why the LXI Device in question does not comply. This will be sent to both the
LXI Device manufacturer and the person or company that initiated the complaint.

If the flaw in the LXI Device is found to be based on a lack of clarity in the specification then the
Conformance Review Committee will forward the matter to the Technical Committee and the
Technical Committee shall initiate a request to update the specification using defined operating
procedures for submitting specification changes.

If the LXI Device is found to be conformant, the matter is finished.

If the LXI Device is found to not be conformant, and if the LXI Device manufacturer agrees in
writing to remedy the situation, the LXI driver manufacturer will be given three months from the
time they are informed of the problem to remedy the situation (either update the LXI Device or
remove claims of conformance).

If the LXI Device manufacturer is not satisfied with the written conclusions of the Conformance
Review Committee, the LXI Device manufacturer may summarize the situation in writing to the
LXI Board of Directors and request they take action on it. The Board of Directors shall review the
findings of the Conformance Review Committee. If it does not agree, a new Conformance Review
Committee will be formed to repeat the work of the previous committee. If the Board of Directors is
in agreement with the Conformance Review Committee that the LXI Device is falsely claiming
conformance to LXI, or falsely using the LXI Consortium logo, the company providing the LXI
Device will be given one month to remedy the problem.

14.4.4 Censure

If the company producing the LXI Device fails to remedy the problem in the prescribed period, the
LXI Board of Directors shall take the following actions:

It shall pass a resolution indicating that the LXI Device is not in compliance and the LXI Device
manufacturer has failed to correct it.

It shall send a letter based on a standard IVI Foundation form to the provider of the LXI Device
stating that the provider is not allowed to use any LXI Consortium trademarks in reference to the
LXI Device in question.

It shall remove the LXI Device's registration information from the publicly available LXI
Consortium website.

At its discretion, the Board of Directors may also remove the LXI Device manufacturer from the
LXI membership or issue a press release stating the situation with the LXI Device manufacturer and
problems with the LXI Device in question.

14.4.5 Closure

All parties involved shall be notified of the results of the process.

If the LXI Device manufacturer subsequently corrects the problem, it may request that the LXI
Consortium update its judgment on the LXI Device.
14.5 Specific Conformance Requirements

14.5.1 Conformance and Interoperability Testing

Interoperability testing shall be conducted either at Plug Fests arranged by the LXI Consortium, or at Interoperability Tests independently arranged by a manufacturer. In either case, the LXI Conformance and Interoperability Test Procedures defined by the Consortium shall be used, and the tests shall be witnessed by a designated representative of the Technical Committee of the Consortium. The specific requirements for Interoperability Testing are defined below.

14.5.1.1 RULE – Conformance Demonstration at a Plug Fest

Manufacturers shall demonstrate conformance of an LXI Device at a Plug Fest arranged by the Consortium.

14.5.1.1.1 Permission – Conformance Demonstration at an Interoperability Test

Manufacturers can alternatively demonstrate conformance of an LXI Device at an Interoperability Test arranged by a member of the Consortium.

14.5.1.2 DEPRECATED RULE – Number of Approved Devices Required for Conformance Testing

This rule is deprecated in favor of Rule 14.5.1.3 because the approved test procedures cover those cases in which multiple devices are needed.

Manufacturers shall demonstrate conformance through interaction with a minimum of one device from each of the three classes. If no commercially available LXI conformant devices exist for a given class, then this requirement may be waived for that class only.

14.5.1.3 RULE – Use of Approved Interoperability Test Procedures

Manufacturers shall use the LXI Conformance and Interoperability Test procedures defined by the Consortium when demonstrating conformance of an LXI Device at a Plug Fest or at an independently arranged Interoperability Test.

14.5.1.4 DEPRECATED RULE – Device Diversity

This rule is deprecated in favor of Rule 14.5.1.3 because the approved test procedures cover those cases in which diversity of devices is needed.

Manufacturers shall test devices against at least one LXI conformant independent implementation to ensure interoperability. Independent implementation may come from an independent design, preferably from another vendor. If no commercially available LXI conformant devices exist for a given class, then this requirement may be waived for that class only.

14.5.1.5 RULE – Witness of the Conformance Tests by a Designated Representative

The conformance tests must be witnessed by a designated representative of the LXI Technical Committee.
14.5.1.6 RULE – Test Documentation and Request for Certification

A Request for Certification for an LXI Device shall be submitted by the manufacturer requesting certification. It shall include the specifications of the LXI Device for which certification is being requested (LXI Conformance Documentation) and the conformance and interoperability test result documentation, including the date of the tests, the version of the LXI Specification which was used for the tests, the version of the Interoperability Test procedures used, the specifications of the other LXI Devices used in the tests, and the test results. Manufacturers will submit a separate Request for Certification request for each device for which certification is requested.

14.5.2 Technical Justification for Conformance

Conformance certification can be requested based on written Technical Justification that a new LXI Device has a direct legacy from and traceability to an existing LXI Device that has already received approval. Reasonable engineering judgment should be exercised when writing the justification document, and the main goal – that of building and selling LXI Devices that interoperate with other LXI Devices without problems – should be kept in mind when taking this course of action. The specific requirements for Interoperability Testing are defined below.

14.5.2.1 RULE – Technical Justification Procedure

The manufacturer requesting LXI Conformance Certification under this section shall submit a Technical Justification Document for the LXI Device for which certification is being requested. It shall include the LXI Conformance Documentation and the conformance and interoperability test result documentation, including the date of the tests, the version of the LXI Specification which was used for the tests of the original devices from which inheritance is claimed, the version of the Interoperability Test procedures used to test the original devices, the specifications of the other LXI Devices used in the tests, and the inheritance of the device or LXI interface of the device for which the certification is being requested. Manufacturers will submit a separate Request for Certification request for each device for which certification is requested.

14.5.3 Certification by an Independent Laboratory

Certification can be requested based on the results of conformance testing carried out by an Independent Laboratory approved by the Consortium.

14.5.3.1 RULE – Test Documentation and Request for Certification

A Request for Certification for an LXI Device shall be submitted by the manufacturer requesting certification. It shall include the specifications of the device for which certification is being requested (LXI Conformance Documentation) and the conformance and interoperability test result documentation, including the name of the manufacturer, device type and serial number, date of the tests, the version of the LXI Specification which was used for the tests, the version of the Interoperability Test procedures used to test the original devices, the specifications of the other LXI Devices used in the tests, the test results, and the name of the independent laboratory, which carried out the tests. Manufacturers will submit a separate Request for Certification request for each device for which certification is requested.
14.6 LXI Device and Documentation Labeling Requirements

14.6.1 LXI Device Labeling

The general intent of these requirements is to ensure that LXI Devices are labeled in a clear manner that allows customers to readily identify the devices, without overly constraining manufacturers' use of corporate colors or technological advances in the use of electronic logos.

14.6.1.1 RULE – Front Panel Labeling Requirements

There shall be an LXI Label on the front of the device with no Class marking. The label shall conform to the specifications of the LXI Consortium in both design and size, and may be either color or monochrome.

14.6.1.2 Permission – Electronic Front Panel Labels

Electronic labels are acceptable instead of a painted or other label on the front of the device. The electronic labels shall be based on the bitmap provided by the LXI Consortium.

14.6.1.3 Recommendation – Device Specifications Label

The manufacturer should provide a label somewhere on the instrument defining the device characteristics and specifications including class, mechanical conformance, etc. The primary source of information on the device shall be provided through the web page in accordance with the specifications contained in Section 9, Web Interface.

14.7 LXI Cables and Terminators Conformance Requirements

14.7.1 RULE - LXI Cables and Terminators Conformance Requirements

LXI cables and terminators shall conform to the rules defined in the LXI Cables and Terminators specification
15 Hybrid Systems

15.1 Introduction

A Hybrid System is a test system comprised of a mix of LXI Devices and other instruments (e.g. GPIB, PXI, VXI, etc.). Types of test systems to consider are:

- A System in which all devices are LXI conformant
- An Aggregate Hybrid System consisting of conformant LXI Devices and other instruments accessed through non-LXI conformant interfaces.
- A Conformant Hybrid System in which all devices are either LXI conformant or are accessed through an LXI conformant Adapter (for non-LXI conformant devices). In a Conformant Hybrid System, all adaptees have been successfully conformance tested with their respective adapters.

Three types of LXI Devices useful in assembling Hybrid Systems are Bridges, Adapters, and Adapter Toolkits:

- A Bridge presents a complete LXI interface on one side and a non-LXI interface on the other (such as GPIB, PCI, PXI, VXI, USB, IEEE 1394, etc.). As such, an LXI bridge is a fully conformant LXI Device (with its own discovery, IVI driver, and web pages—whose functions relate to the operation of the bridge), independent of the devices on the far side. All devices on the far side of the bridge are not LXI conformant. The intended use of a bridge is to help integrators of Aggregate Hybrid Systems incorporate non-LXI conformant devices in a manner similar or akin to LXI conformant devices. The Bridge is tested for conformance as a stand-alone device.

- An Adapter presents a complete LXI interface for one or more adaptees represented as an LXI Device or Devices in a Conformant Hybrid System. The intended use of an Adapter is to provide LXI conformance for its adaptees. Therefore, the Adapter and designated adaptees are conformance tested together as a set.

- An Adapter Toolkit is a hardware interface with a software development kit that, like an Adapter, presents a complete LXI interface for one or more adaptees represented as a LXI Device or Devices in a Conformant Hybrid System. However, unlike an Adapter, the Adapter Toolkit allows the integrator to select the adaptees and complete the customizations required to make the set LXI conformant. The intended use of an Adapter Toolkit is to help integrators of Conformant Hybrid Systems incorporate non-LXI conformant devices in a manner indistinguishable from LXI conformant devices. The Adapter Toolkit is certified and carries the LXI logo. Although the Adapter Toolkit may be used to develop an LXI interface to another adaptee that is functionally complete and which can be used in a Hybrid System, the Adapter Toolkit and adaptee cannot be considered conformant until they have passed certification testing as a set.

Bridges, Adapters, and Adapter Toolkits are conformance tested like all other LXI Devices and are, therefore, eligible to carry the LXI logo. The associated devices (adaptees and downstream devices attached to a bridge) are not eligible to carry the LXI logo; the only case where this can occur is if the Adapter and the adaptee are in the same physical package (i.e., the Adapter is contained within the adaptee).
15.2 RULE – Adapter and Adaptee(s) conformance tested together

An adapter shall be conformance tested with its adaptee(s) so that proper discovery, web page, and driver operation can be verified. The adapter-adaptee set is then considered LXI conformant.

15.3 RULE – Adapter Toolkits and Adaptees conformance tested together

An Adapter Toolkit shall be conformance tested with at least one adaptee so that proper discovery, web page, and driver operation can be verified. The Adapter Toolkit is then considered LXI conformant.

15.3.1 Permission – A NULL device may be used as an adaptee

An Adapter Toolkit may be conformance tested using a NULL device as an adaptee. The NULL device allows the Adapter Toolkit to reflect itself at the LXI interface—thus the discovery response, IVI driver, and web page content are only pertinent to the Adapter Toolkit. Any Adapter Toolkit so tested cannot claim to offer LXI conformance on any particular adaptee until it is customized or configured for the adaptee.

15.4 Future Roadmap Topics

15.4.1 Adaptee Conformance

The HSWG will pursue the possibility that adaptee devices attached to Adapter Toolkits can be given full conformance status.

15.4.2 Adapter Toolkits – Software Only Applications

The HSWG will pursue the possibility that Adapter Toolkits can take the form of a software-only application and be marketed in the absence of any hardware.

15.4.3 Many to Many Implementations

The HSWG will pursue the possibility that multiple non-LXI Devices can be adapted as multiple LXI Devices through a single Adapter (Many to Many Use case).
Appendix A  Sample Web Pages
Advanced IP Configuration

Auto-Negotiation: AutoSelect

ECMP Ring
- Disabled
- Enabled

SNMP Status
- Disabled
- Enabled

SNMP Public Community: public

SNMP Private Community: private

SNMP Trap IP Address: 192.168.1.5

RMON Discovery
- Disabled
- Enabled

VXI-11 Discovery
- Disabled
- Enabled

Submit  Reset

Simple IP Configuration

Synchronization Configuration

IEEE-1588
- Disabled
- Enabled

Grandmaster Clock: 192.168.1.110

Parent Clock: 192.168.1.110

Stats
- Slave

Current RTP Time: 2003/06/09

Current Local Time: 11:47:51.173305 seconds - 3rd April 2003

Trace Log Level
- VFC

Interface Statistics
- 50 mbps

IEEE 1588 Domain: 0

LXI Module-to-Module Parameters:

- LXI Domain: 0

Wired Trigger Parameters:
- Wired-Or Edge LX10: Disabled
- Wired-Or Edge LX1: Disabled
- Wired-Or Edge LX2: Disabled
- Wired-Or Edge LX3: Disabled
- Wired-Or Edge LX4: Disabled
- Wired-Or Edge LX5: Disabled
- Wired-Or Edge LX6: Disabled
- Wired-Or Edge LX7: Disabled

Submit  Reset
Appendix B  Module-to-Module (LAN Event) Packet Examples

Table B.1 gives several examples of LAN event packets.

Note: The packet is terminated by a data length field with a value of zero (0x0000).

Note: All multi-octet fields are transmitted as big-endian.

Table B.1 – LAN Event Packet Examples

<table>
<thead>
<tr>
<th>Packet Header (ASCII)</th>
<th>Domain</th>
<th>Identifier</th>
<th>Sequence Number</th>
<th>Seconds</th>
<th>Nanoseconds</th>
<th>Fractional Nanoseconds</th>
<th>Epoch</th>
<th>Flags</th>
<th>HDWR Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>LXI 0x00  LAN0  0x1357feff 0x00000002 0x00000111 0x0000  0x0000</td>
<td>3 Octets (uint8)</td>
<td>1 Octet (uint8)</td>
<td>16 Octets null padded</td>
<td>4 Octets (uint32)</td>
<td>4 Octets (uint32)</td>
<td>2 Octets (uint16)</td>
<td>2 Octets (uint16)</td>
<td>0x0004 HDWR Value = TRUE</td>
<td></td>
</tr>
<tr>
<td>LXI 0x00  LAN5  0x12345678 0x00000002 0x80000000 0x0000  0x0000</td>
<td>3 Octets (uint8)</td>
<td>1 Octet (uint8)</td>
<td>16 Octets null padded</td>
<td>4 Octets (uint32)</td>
<td>4 Octets (uint32)</td>
<td>2 Octets (uint16)</td>
<td>2 Octets (uint16)</td>
<td>0x0004 HDWR Value = TRUE</td>
<td></td>
</tr>
<tr>
<td>LXI 0x01  LAN3  0xff000539 0x463682c3 0x1dcd6500 0x0000  0x0000</td>
<td>3 Octets (uint8)</td>
<td>1 Octet (uint8)</td>
<td>16 Octets null padded</td>
<td>4 Octets (uint32)</td>
<td>4 Octets (uint32)</td>
<td>2 Octets (uint16)</td>
<td>2 Octets (uint16)</td>
<td>0x0008 ACK &amp; HDWR Value = FALSE</td>
<td></td>
</tr>
<tr>
<td>LXI 0x95  LXIERROR 0x00000001 0x463682c3 0x1abc6500 0x0000  0x0000</td>
<td>3 Octets (uint8)</td>
<td>1 Octet (uint8)</td>
<td>16 Octets null padded</td>
<td>4 Octets (uint32)</td>
<td>4 Octets (uint32)</td>
<td>2 Octets (uint16)</td>
<td>2 Octets (uint16)</td>
<td>0x0001 Error</td>
<td></td>
</tr>
</tbody>
</table>
Table B.2 illustrates usage of the data fields.

Note: All LAN event packets must be terminated by an empty data field – that is, one with a Data Length field with a value of zero and no Identifier or User Data field.

### Table B.2 – Usage

<table>
<thead>
<tr>
<th>Data Length (2 octets)</th>
<th>Identifier (1 octet)</th>
<th>User Data (Data Length octets; encoded in hexadecimal)</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>0x0008</td>
<td>0x04</td>
<td>0102 0304 0506 0708</td>
<td>User-defined data type</td>
</tr>
<tr>
<td>0x0011</td>
<td>0xFF</td>
<td>5468 6973 2069 7320 6120 7374 7269 6E67 2E</td>
<td>The ASCII string, “This is a string.”</td>
</tr>
<tr>
<td>0x0008</td>
<td>0xFC</td>
<td>0102 1112 2122 3132</td>
<td>Four int16’s</td>
</tr>
<tr>
<td>0x0000</td>
<td></td>
<td></td>
<td>Packet Terminator</td>
</tr>
</tbody>
</table>

The octet stream for the LAN event in the first row of Table B.1 containing all of the data fields of Table B.2 and encoded in hexadecimal would be the following:

```
4C58 4900 4C41 4E30 0000 0000 0000 0000 0000 0000 1357 FEFF 0000 0002 0000 0111 0000 0000 0004 0008 0401 0203 0405 0607 0800 11FF 5468 6973 2069 7320 6120 7374 7269 6E67 2E00 08FC 0102 1112 2122 3132 0000
```

That is:

<table>
<thead>
<tr>
<th>Octets</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>4C58 49</td>
<td>LXI</td>
</tr>
<tr>
<td>00</td>
<td>Domain = 0</td>
</tr>
<tr>
<td>4C41 4E30 0000 0000 0000 0000 0000 0000</td>
<td>Event ID = “LAN0”</td>
</tr>
<tr>
<td>1357 FEFF</td>
<td>Sequence Number</td>
</tr>
<tr>
<td>0000 0002</td>
<td>Seconds</td>
</tr>
<tr>
<td>0000 0111</td>
<td>Nanoseconds</td>
</tr>
<tr>
<td>0000</td>
<td>Fractional Nanoseconds</td>
</tr>
<tr>
<td>0004</td>
<td>Epoch</td>
</tr>
<tr>
<td>0008</td>
<td>Flags (Hardware Value = True)</td>
</tr>
<tr>
<td>04</td>
<td>Data Length = 8</td>
</tr>
<tr>
<td>0102 0304 0506 0708</td>
<td>Identifier (user-defined)</td>
</tr>
<tr>
<td>0011</td>
<td>User Data</td>
</tr>
<tr>
<td>FF</td>
<td>Data Length = 17</td>
</tr>
<tr>
<td>5468 6973 2069 7320 6120 7374 7269 6E67 2E</td>
<td>Identifier (0xFF – String)</td>
</tr>
<tr>
<td>0008</td>
<td>User Data (“This is a string.”)</td>
</tr>
<tr>
<td>FC</td>
<td>Data Length = 8</td>
</tr>
<tr>
<td>0102 1112 2122 3132</td>
<td>Identifier (0xFC – int16)</td>
</tr>
<tr>
<td>0000</td>
<td>User Data</td>
</tr>
<tr>
<td>0000</td>
<td>Data Length = 00 / Packet Terminator</td>
</tr>
</tbody>
</table>
Appendix C  Example Identification Documents

The following XML files are example instances of the LXI Identification and its extension (available at http://www.lxistandard.org/InstrumentIdentification/1.0).

Identification Document

Example Identification Document conforming to LXI InstrumentIdentification that illustrates ConnectedDevices, use of the Extension element for vendor-specific data, and the schemalocation attribute:

```xml
<?xml version="1.0" encoding="UTF-8"?>
<LXIDevice xmlns="http://www.lxistandard.org/InstrumentIdentification/1.0"
   xmlns:xsi="http://www.w3.org/2001/XMLSchema-instance"
   xsi:schemaLocation="http://www.lxistandard.org/InstrumentIdentification/1.0
   http://sampledevice.local/static/LXIIdentification.xsd">
   <Manufacturer>My Company, Inc.</Manufacturer>
   <Model>EX1234</Model>
   <SerialNumber>543210</SerialNumber>
   <FirmwareRevision>1.2.3a</FirmwareRevision>
   <ManufacturerDescription>Sample Device</ManufacturerDescription>
   <HomepageURL>http://www.mycompany.com</HomepageURL>
   <DriverURL>http://www.mycompany.com</DriverURL>
   <ConnectedDevices>
      <DeviceURI>http://sampledevice.local/devices/device0/</DeviceURI>
      <DeviceURI>http://sampledevice.local/devices/device2/</DeviceURI>
   </ConnectedDevices>
   <UserDescription>Demo of Identification Schema</UserDescription>
   <IdentificationURL>http://sampledevice.local/lxi/identification</IdentificationURL>
   <Interface xsi:type="NetworkInformation" InterfaceType="LXI" IPType="IPv4"
   InterfaceName="eth0">
      <InstrumentAddressString>TCPIP::10.1.2.32::INSTR</InstrumentAddressString>
      <InstrumentAddressString>TCPIP::10.1.2.32::5000::SOCKET</InstrumentAddressString>
      <Hostname>10.1.2.32</Hostname>
      <IPAddress>10.1.2.32</IPAddress>
      <SubnetMask>255.255.255.0</SubnetMask>
      <MACAddress>00:3F:F8:6A:1A:3A</MACAddress>
      <Gateway>10.1.2.1</Gateway>
      <DHCPEnabled>True</DHCPEnabled>
      <AutoIPEnabled>True</AutoIPEnabled>
   </Interface>
   <Interface InterfaceType="MyCompanyCustomNetworkInterface"
   InterfaceName="MyCompany1">
      <InstrumentAddressString>10.1.2.32:5025</InstrumentAddressString>
</LXIDevice>
```

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<Interface>
<IVISoftwareModuleName>Thingamajig</IVISoftwareModuleName>
<Extension>
  <SampleExtension>
    Arbitrary Vendor Extension Data can go here.
  </SampleExtension>
</Extension>
<LXIClass>A</LXIClass>
<Domain>1</Domain>
<LXIVersion>1.2</LXIVersion>
</LXIDevice>

Derived Schema

The following is an identification schema (XSD) that derives from the LXI InstrumentIdentification schema and is used for the two ConnectedDevices in the above sample Identification document. The schema creates a new element “MyDevice” that uses the “Device” element of the LXI InstrumentIdentification schema as a base to extend. The new element contains only one additional element beyond that defined by “lxi:Device”: LogicalAddress, which is an unsigned byte.

```xml
<?xml version="1.0" encoding="UTF-8"?>
<xs:schema targetNamespace="http://www.mycompany.com/MyIdentification/1.0"
  xmlns:xs="http://www.w3.org/2001/XMLSchema"
  xmlns:lxi="http://www.lxistandard.org/InstrumentIdentification/1.0"
  elementFormDefault="qualified"
  attributeFormDefault="unqualified">
  <xs:import namespace="http://www.lxistandard.org/InstrumentIdentification/1.0"
    schemaLocation="http://sampledevice.local/static/LXIIdentification.xsd"/>
  <xs:element name="MyDevice">
    <xs:annotation>
      <xs:documentation>An example identification of a device based on the generic LXI Model</xs:documentation>
    </xs:annotation>
    <xs:complexType>
      <xs:complexContent>
        <xs:extension base="lxi:Device">
          <xs:sequence>
            <xs:element name="LogicalAddress" type="xs:unsignedByte"/>
          </xs:sequence>
        </xs:extension>
      </xs:complexContent>
    </xs:complexType>
  </xs:element>
</xs:schema>
```

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Connected Devices

The sample Identification Document above contains two ConnectedDevice URIs. The identification documents for these two devices may be queried by appending “lxi/identification” to the URIs provided. These connected devices are instances of the sample MyIdentification Schema defined above. Note that they reference both the LXI InstrumentIdentification Schema as well as the derived MyIdentificationSchema in the schemalocation attribute.

The first device’s identification document’s URL is http://sampledevice.local/devices/device0/lxi/identification. The document’s contents are:

```xml
<?xml version="1.0" encoding="UTF-8"?>
<MyDevice xmlns="http://www.mycompany.com/MyIdentification/1.0"
  xmlns:lxi="http://www.lxistandard.org/InstrumentIdentification/1.0"
  xmlns:xsi="http://www.w3.org/2001/XMLSchema-instance"
  xsi:schemaLocation="http://www.lxistandard.org/InstrumentIdentification/1.0
  http://sampledevice.local/static/LXIIdentification.xsd
  http://www.mycompany.com/MyIdentification/1.0
  http://sampledevice.local/static/MyIdentification.xsd" >

  <lxi:Manufacturer>My Company, Inc.</lxi:Manufacturer>
  <lxi:Model>1234</lxi:Model>
  <lxi:SerialNumber>123</lxi:SerialNumber>
  <lxi:FirmwareRevision>1.2.3a</lxi:FirmwareRevision>
  <lxi:Interface InterfaceType="MyCompanyProprietary" InterfaceName="instr0">
    <lxi:InstrumentAddressString>TCPIP::10.1.2.32::inst1::INSTR</lxi:InstrumentAddressString>
  </lxi:Interface>
  <lxi:Extension>
    <MySampleDeviceExtension>
      Arbitrary Vendor Extension Data can go here.
    </MySampleDeviceExtension>
  </lxi:Extension>
  <LogicalAddress>0</LogicalAddress>
</MyDevice>
```

The second device’s identification document’s URL is http://sampledevice.local/devices/device2/lxi/identification. The document’s contents are:

```xml
<?xml version="1.0" encoding="UTF-8"?>
<MyDevice xmlns="http://www.mycompany.com/MyIdentification/1.0"
  xmlns:lxi="http://www.lxistandard.org/InstrumentIdentification/1.0"
  xmlns:xsi="http://www.w3.org/2001/XMLSchema-instance"
  xsi:schemaLocation="http://www.lxistandard.org/InstrumentIdentification/1.0
  http://sampledevice.local/static/LXIIdentification.xsd
  http://www.mycompany.com/MyIdentification/1.0
  http://sampledevice.local/static/MyIdentification.xsd" >

  <lxi:Manufacturer>My Company, Inc.</lxi:Manufacturer>
  <lxi:Model>1234</lxi:Model>
  <lxi:SerialNumber>123</lxi:SerialNumber>
  <lxi:FirmwareRevision>1.2.3a</lxi:FirmwareRevision>
  <lxi:Interface InterfaceType="MyCompanyProprietary" InterfaceName="instr0">
    <lxi:InstrumentAddressString>TCPIP::10.1.2.32::inst1::INSTR</lxi:InstrumentAddressString>
  </lxi:Interface>
  <lxi:Extension>
    <MySampleDeviceExtension>
      Arbitrary Vendor Extension Data can go here.
    </MySampleDeviceExtension>
  </lxi:Extension>
  <LogicalAddress>0</LogicalAddress>
</MyDevice>
```
<lxi:Manufacturer>My Company, Inc.</lxi:Manufacturer>
<lxi:Model>1234</lxi:Model>
<lxi:SerialNumber>456</lxi:SerialNumber>
<lxi:FirmwareRevision>1.2.3a</lxi:FirmwareRevision>
<lxi:Interface InterfaceType="MyCompanyProprietary" InterfaceName="instr2">
  <lxi:InstrumentAddressString>TCPIP::10.1.2.32::inst2::INSTR</lxi:InstrumentAddressString>
  <lxi:InstrumentAddressString>TCPIP::10.1.2.32::3002::SOCKET</lxi:InstrumentAddressString>
</lxi:Interface>
<lxi:Extension>
  <MySampleDeviceExtension>
    Arbitrary Vendor Extension Data can go here.
  </MySampleDeviceExtension>
</lxi:Extension>
<lxi:LogicalAddress>2</lxi:LogicalAddress>
</MyDevice>
Appendix D  Glossary of Terms

API

API stands for Application Programming Interface.

Auto-MDIX

Auto-MDIX is a protocol which allows two Ethernet devices to negotiate their use of the Ethernet TX and RX cable pairs. This allows two Ethernet devices with MDI-X or MDI connectors to connect without using a crossover cable. This feature is also known as Auto-crossover.

ARP

The address resolution protocol (ARP) is a protocol used by the Internet Protocol (IP), specifically IPv4, to map IP network addresses to the hardware addresses used by a data link protocol. It is used when IPv4 is used over Ethernet. The term address resolution refers to the process of finding an address of a computer in a network.

Default gateway

A configuration item for the TCP/IP protocol that is the IP address of a directly reachable IP router. Configuring a default gateway creates a default route in the IP routing table.

DHCP

See definition for: Dynamic Host Configuration Protocol (DHCP)

DNS

See definition for: Domain Name System (DNS)

DNS server

A server that maintains information about a portion of the Domain Name System (DNS) database and that responds to and resolves DNS queries.

Domain name

The name given by an administrator to a collection of networked computers that share a common directory. Part of the Domain Name System (DNS) naming structure, domain names consist of a sequence of name labels separated by periods.

Dynamic Host Configuration Protocol (DHCP)

The Dynamic Host Configuration Protocol provides a framework for passing configuration information to hosts on a TCP/IP network. DHCP is based on the Bootstrap Protocol (BOOTP), adding the capability of automatic allocation of reusable network addresses and additional configuration options. DHCP captures the behavior of BOOTP relay agents, and DHCP participants can interoperate with BOOTP participants. DHCP provides safe, reliable, and simple TCP/IP network configuration, prevents address conflicts, and helps conserve the use of client IP addresses on the network.
DHCP uses a client/server model where the DHCP server maintains centralized management of IP addresses that are used on the network. DHCP-supporting clients can then request and obtain lease of an IP address from a DHCP server as part of their network boot process.

Hostname

A hostname is the unique name by which a network attached device is known on a network. The hostname is used to identify a particular host in various forms of electronic communication such as E-mail or Usenet.

HTML

See definition for: Hypertext Markup Language (HTML)

HTTP

See definition for: Hypertext Transfer Protocol (HTTP)

Hypertext Markup Language (HTML)

A simple markup language used to create hypertext documents that are portable from one platform to another. HTML files are simple ASCII text files with codes embedded (indicated by markup tags) to denote formatting and hypertext links.

Hypertext Transfer Protocol (HTTP)

The protocol used to transfer information on the World Wide Web. An HTTP address (one kind of Uniform Resource Locator [URL]) takes the form: http://www.w3.org.

ICMP

Internet Control Message Protocol (ICMP) is a required protocol tightly integrated with IP. ICMP messages, delivered in IP packets, are used for out-of-band messages related to network operation or mis-operation.

IEEE

Institute of Electrical and Electronics Engineers. A global technical professional society and standards-setting organization serving the public interest and its members in electrical, electronics, computer, information and other technologies.

IEEE 1588 (PTP)

IEEE 1588 is a standard for a precision clock synchronization protocol for networked measurement and control systems. It is also known as the Precision Time Protocol (PTP).

Front Panel User Interface

A front panel user interface is defined as consisting of control and displays functions, located on the front panel of a device that can be used to set up critical aspects of the LXI interfaces and instrument operation.

Internet Protocol (IP)

A routable protocol in the TCP/IP protocol suite that is responsible for IP addressing, routing, and the fragmentation and reassembly of IP packets.
**IP**

See definition for: Internet Protocol (IP)

**IP address**

An address used to identify a node on an IP internetwork. Each node on the IP internetwork must be assigned a unique IP address, which is made up of the network ID, plus a unique host ID. This address is typically represented with the decimal value of each octet separated by a period (for example, 192.168.7.27). You can configure the IP address statically or dynamically by using DHCP.

**IVI**

IVI stands for Interchangeable Virtual Instrument. The IVI Foundation is an open consortium founded to promote specifications for programming test instruments that simplify interchangeability, provide better performance, and reduce the cost of program development and maintenance.

**LAN**

See definition for: local area network (LAN)

**LCI**

LAN Configuration Initialize (LCI) is a LXI devices recessed reset mechanism (e.g., a button) on the rear or front of the module that when activated places the module’s network settings to a default state.

**Local Area Network (LAN)**

A communications network connecting a group of computers, printers, and other devices located within a relatively limited area (for example, a building). A LAN allows any connected device to interact with any other on the network.

**LVDS**

LVDS stands for Low-Voltage Differential Signaling.

**LXI**

LXI stands for LAN eXtensions for Instruments. LXI is the next generation instrumentation platform based on industry standard Ethernet technology and provides modularity, flexibility and performance to small- and medium-sized systems.

**LXI Identification XSD Schema**

An XML Schema that conforms to XSD standards and is defined by the LXI Consortium to specify XML documents that provide identification information about LXI devices.

**LXI Unit**

Half-width rack mounted LXI devices.
M-LVDS

Multipoint Low-Voltage Differential Signaling conforming to the TIA/EIA-899 standard, which allows multiple transmitters and receivers to be interconnected on a single, balanced, doubly-terminated media pair. Multipoint operation allows for bidirectional, half-duplex communication between multiple devices connected to the same transmission line.

M-LVDS Type-1

One of two classes of M-LVDS receivers, having a differential input voltage threshold centered about zero volts. Differential input signals below -50 mV are defined by the TIA/EIA-899 standard to be in the low state, and signals above +50 mV are defined to be in the high state. When the input of a Type-1 receiver is connected to an undriven twisted pair, the differential input voltage is defined to be in the threshold transition region. This condition will result in a stable, but undefined, output.

MAC

See definition for: media access control

MAC Address

Media Access Control address. A unique hardware number that identifies each device on a network. A device can be a Instrument, computer, printer, etc.

Media Access Control (MAC)

A sublayer of the IEEE 802 specifications that defines network access methods and framing.

MIB

Short for Management Information Base, a database of objects that can be monitored by a network management system. Both SNMP and RMON use standardized MIB formats that allows any SNMP and RMON tools to monitor any device defined by a MIB.

Ping

A utility that verifies connections to one or more remote hosts. The ping command uses the ICMP echo request and echo reply packets to determine whether a particular IP system on a network is functional. Ping is useful for diagnosing IP network or router failures.

PoE

IEEE 802.11f Power Over Ethernet is a technology for wired Ethernet LAN that allows the electrical current, necessary for the operation of each device, to be carried by the CAT5 data cables instead of a traditional power cord.

PTP

See definition for IEEE 1588.

Schema

A document that describes a language or parameters of a language. Thus, XML Schemas provide a means of describing the structure, content, and semantics of XML documents.
**SCPI**

The Standard Commands for Programmable Instrumentation (SCPI) defines a standard set of commands to control programmable test and measurement devices in instrumentation systems. The SCPI Standard is built on the foundation of IEEE-488.2, Standard Codes and Formats.

**Simple Network Management Protocol (SNMP)**

A network protocol used to manage TCP/IP networks. In Windows, the SNMP service is used to provide status information about a host on a TCP/IP network.

**SNMP**

See definition for: Simple Network Management Protocol (SNMP)

**Star Hub**

An LXI Device that can be used to connect Wired Trigger Bus chains together, in doing so providing electrical isolation between the chains. They can be used to extend the maximum number of LXI devices that can participate in a wired trigger event by providing a mapping function between the Wired Trigger Bus chains. Star Hubs can be stand alone devices or can be embedded LXI devices having other functionality. They are the only LXI device that is permitted to have an embedded termination for a Wired Trigger Bus.

**Subnet**

A subdivision of an IP network. Each subnet has its own unique subnetted network ID.

**Subnet Mask**

A 32-bit value that enables the recipient of IP packets to distinguish the network ID and host ID portions of the IP address. Typically, subnet masks use the format 255.x.x.x.

**TCP/IP**

See definition for: Transmission Control Protocol/Internet Protocol (TCP/IP)

**Transmission Control Protocol/Internet Protocol (TCP/IP)**

A set of networking protocols widely used on the Internet that provides communications across interconnected networks of computers with diverse hardware architectures and various operating systems. TCP/IP includes standards for how computers communicate and conventions for connecting networks and routing traffic.

**UDP**

The User Datagram Protocol (UDP) is one of the core protocols of the Internet protocol suite. Using UDP, programs on networked computers can send short messages known as datagrams to one another.

**Uniform Resource Locator (URL)**

An address that uniquely identifies a location on the Internet. Generally an URL specifies the connection protocol and a file name. The connection protocol can be: telnet, ftp, gopher, etc., and for web pages, http is the usual protocol as in the fictitious URL http://www.example.microsoft.com.

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URL

See definition for Uniform Resource Locator (URL)

UTC

Coordinated Universal Time (abbreviated UTC) is the basis for the worldwide system of civil time. This time scale is kept by time laboratories around the world, including the U.S. Naval Observatory, and is determined using highly precise atomic clocks.

VISA

Most of the instrument drivers communicate to the instrumentation hardware through an I/O Library. The VISA library is used for the GPIB, VXI, PXI, Serial, Ethernet, and/or USB interfaces, while other buses can either utilize VISA or another library.

W3C

The World Wide Web Consortium (W3C) develops interoperable technologies (specifications, guidelines, software, and tools) to lead the Web to its full potential as a forum for information, commerce, communication, and collective understanding.

XSD

An XML Schema Definition, as defined by the W3C (http://www.w3.org/XML/Schema). It defines a type of XML document in terms of the constraints upon what elements and attributes may appear, their relationship to each other, what types of data may be in them, and so forth.